**Project 5**

Shuo Shang, Jingyi Wang, Ruixin Wang

(a) What did you learn from this project?

(b) What would you do differently next time?

(c) What is your advice to someone who is going to work on a similar project?

(a) In this project, I have learnt the basic knowledge of designing a CPU. When designing a CPU, we have to design the Instruction Set Architecture first, and the datapath and control unit are designed according to the well designed ISA. This project helped me to review some knowledge that were taught in the class, so we got a deeper understanding of the logic relationships between all instruction sets, and the theory of datapath and control unit. And we don’t have enough time to do the project, so we should spend more time or start earlier in this project.

(b) The control unit of our project is not perfectly done, so we have design the control unit better. Besides, the ISA is not well designed, so in the next time, we would like to design a better ISA, so that it would be more convenient for us to design the datapath and control unit.

(c) Since all the five parts of this project is firmly connected, you have to design this project well organized from the first part. Before design the project, the theory of CPU must be clearly understood. And to start earlier and spend more time is very important