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ELEC 6200 – CPU design project - Report 5

(a) What did you learn from this project?

We have learned how to design and implement all the individual components of the CPU. We have learnt using the Altera FPGA DE board .This project helped us in improving our VHDL coding skills and get familiarized to the tools Modelsim and Quartus II. Most importantly, it helped us understand completely the organization of the RISC**.**

(b) What would you do differently next time?

In case we had to redo the project we would have started using the Altera board earlier in the project. There were some last minute problems that have crept in while implementing the memory instructions. I would have made sure that all the instructions would’ve worked perfectly well.

(c) What is your advice to someone who is going to work on a similar project?

We would recommend one to start the project at least a month prior to the submission as one would encounter a lot of snags. This would help in overcoming the several issues that one might encounter while doing the physical implementation