
Test Technology Newsletter

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*The Newsletter of the Test Technology Technical Council
of the IEEE Computer Society*



Editor: Theo Theocharides

PAST TTTC EVENTS

The IEEE International Test Conference (ITC 2021)

10–15 October, 2021

Virtual Live Event

<http://www.itctestweek.org/about-itc/>

International Test Conference is the world's premier venue dedicated to the electronic test of devices, boards and systems—covering the complete cycle from design verification, design-for-test, design-for-manufacturing, silicon debug, manufacturing test, system test, diagnosis, reliability and failure analysis, and back to process and design improvement. At ITC, design, test, and yield professionals can confront challenges faced by the industry and learn how these challenges are being addressed by the combined efforts of academia, design tool and equipment suppliers, designers, and test engineers. ITC, the cornerstone of the Test Week event, offers a wide variety of technical activities targeted at test and design theoreticians and practitioners, including: formal paper sessions, tutorials, panel sessions, case studies, invited lectures, commercial exhibits and presentations, and a host of ancillary professional meetings.

UPCOMING TTTC EVENTS

The 25th Design, Automation, and Test in Europe (DATE) Conference

14–23 March, 2022

Antwerp, BELGIUM and Virtual Online

<https://www.date-conference.com/>

The 25th DATE conference is the main European event bringing together designers and design automation users, researchers and vendors, as well as specialists in the hardware and software design, test and manufacturing of electronic circuits and systems. DATE puts a strong emphasis on both technology and systems, covering ICs/SoCs,

reconfigurable hardware and embedded systems, and embedded software. The multi-day event consists of a conference with regular papers, complemented by panels, hot-topic sessions, tutorials, workshops, special focus days and executives. The event will also host the Young People Program, the University Fair and Multi-Partner Projects dissemination on innovative research activities fostering the networking and the exchange of information on relevant issues, re-cent research outcomes and career opportunities. DATE 2022 is the 25th edition of an event that has been the place for researchers, young professionals, and industrial partners to meet, present their research, and discuss the current development and next trends, with high emphasis on social interaction. For its 2022 event, DATE presents a special format, as the situation related to COVID-19 is improving but safety measures and restrictions will remain uncertain for the upcoming months across Europe and worldwide. In transition towards a future post-pandemic event again, DATE 2022 will host a two-day live event in presence in the city of Antwerp, to bring the community together again, followed by other activities carried out entirely online in the subsequent days. This setup combines the in-presence experience with the opportunities of online activities, fostering the networking and social interactions around an interesting program of selected talks and panels on emerging topics to complement the traditional DATE high-quality scientific, technical and educational activities.

The IEEE VLSI Test Symposium

25–27 April, 2022

San Diego, US

https://tttc-vts.org/public_html/new/2022/

The IEEE VLSI Test Symposium (VTS) explores emerging trends and novel concepts in testing, debug and repair of microelectronic circuits and systems.

The VTS Program Committee invites original, unpublished paper submissions for VTS 2022. Proposals for the

innovative practices and special sessions tracks are also invited. The program includes keynotes, scientific paper presentations, short industrial application paper presentations, special sessions, and Innovative Practices sessions.

VTS puts particular emphasis on enlarging its scope soliciting submissions on aspects on the following hot topics: Test, Reliability, and Security of Heterogeneous Devices Including Biomedical, Neuromorphic, and MEMS, Test & Reliability of Machine Learning Systems, Test, Reliability & Security in Quantum Computing, Design and Test Methods for Yield Improvement, Machine Learning for Test, Debug, and Yield Improvement and Reliable in-Field Calibration of mm-Wave Devices.

The 2022 edition of VTS will be held in San Diego or as an online virtual interactive live event depending on the evolution of the COVID-19 worldwide situation.

The 27th IEEE European Test Symposium (ETS'22)

23 – 27 May, 2022

Barcelona, Spain

<http://ets2021.upc.edu/en/>

The IEEE European Test Symposium (ETS) is Europe's premier forum dedicated to presenting and discussing scientific results, emerging ideas, applications, hot topics and new trends in the area of electronic-based circuits and system testing, reliability, security and validation. ETS'22 will be held in Casa Convalescència, located in the historical modernist site Hospital de la Santa Creu i Sant Pau (World Heritage Site by UNESCO, 1997) in Barcelona, which is located 10 min walking from the Sagrada Família. The format of the conference will be hybrid, please check "COVID19" menu for more details. ETS'22 is organized by UPC (Universitat Politècnica de Catalunya).

The program includes keynotes, scientific paper presentations, panels, tutorials, workshops and highlights/demos from industry. Besides regular technical papers, ETS'22 provides the opportunity of submitting scientific contributions

for hot-topic papers and case-study papers (each with specific evaluation criteria). Submissions are also solicited for special sessions, tutorials, panels and workshops, as well as for the PhD Forum. Linked to the main ETS'22 symposium, the Test Spring School and Fringe Workshops will be organized.

NEWSLETTER EDITOR'S INVITATION

I would appreciate input and suggestions about the newsletter from the test community. Please forward your ideas, contributions, and information on awards, conferences, and workshops to Theocharis (Theo) Theocharides, Department of Electrical and Computer Engineering, University of Cyprus, 75 Kallipoleos Avenue, PO Box 20537, Nicosia, 1678, CYPRUS; ttheocharides@ucy.ac.cy.

Theo Theocharides.

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CONTRIBUTIONS TO THIS NEWSLETTER:

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