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ELEC 5200

Project Part 6: Final Report

1. **What did you learn from this project?**

This project was very interesting from previous projects. We’ve done projects in the past using assembly language and vhdl, and we’ve done projects using microprocessors. This project was different because this time, we didn’t use a microprocessor, but created our own! We also designed and developed our very own assembly language, granted it was similar to MIPS assembly language. We learned not how to use a microprocessor, but how a microprocessor works and the datapath and memory/register control within it.

1. **What would you do differently next time?**

There were a few things we would like to have done differently. We would probably consider pipelining instead of a single cycle datapath to make testing easier. Also, we definitely feel like we could have planned better about interfacing external memories, and planned better about the instructions in binary in order to keep them simple.

1. **What is your advice to someone who is going to work on a similar project?**

The only advice we could think to give is to pay attention in class, because it will come a long way in the project. The project parts move smoothly along with the lectures. Oh, and definitely read all parts of the project first. It will help in the early stages to anticipate what’s coming up in the future parts. The last piece of advice is to use your time wisely, and do not wait till the end of the week to get to work. If you have the time to meet, then meet.