**ELEC 7950-001: VLSI Design & Test Seminar**

**Broun 235, October 5, 2011, 4PM**

**Computer-Aided ASIC Design: Concept to Silicon**

**Victor Nelson**

***Abstract:*** This seminar will overview the design flow for the development of application-specific integrated circuits (ASICs), including behavioral modeling, automated synthesis, design for testability, test generation, physical design, and design verification, including simulation of the design at each step of the design flow. The computer-aided design tools from Mentor Graphics, Synopsys, and Cadence, available in the ECE Department, will be described as applicable to the various steps in the design flow.

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