Computer A&D Term Project Part 6

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1. **What did I learn**

The project tell me the implementation and inner design of the CPU. Design the instruction set, how the data path of the cpu works, VHDL language and basic operations of Quartus and FPGA board.

On designing and debug step, I learned how to deal with the flaw I met and the way to solve it.

1. **What would I do different next time**

I may try to design different data path, especially the pipeline data-path, which would help me understand better about the hazard.

Also, I may try to simplify the state table, make the program executed faster.

Furthermore, I may try to learn more about the FPGA and Quartus software.

1. **My advice to the one who is going for this project**

First, you might want do is as earlier as possible, because it will takes more time than you imagine.

Second, try to do that all by yourself, it will take you more time, but you will learn a whole new world, a lot of things not only the coding but more important the debugging work.

Seriously, be patients.