JTAG Interface Header

Jumper Block Storage

FPGA Mode Select Jumpers

Notes: A shorting block must be installed between TDI and TDO on J7 to complete the scan chain when the JTAG signals are being driven from a peripheral board position rather than the JTAG Interface Header.

Shorting blocks are installed on pins 2-3 of JP1 and JP2 to bypass the serial configuration ROM (ICB).

Shorting blocks should be installed on MODE0 and MODE1 of J8 to select Slave Serial Mode when configuring from the ROM.