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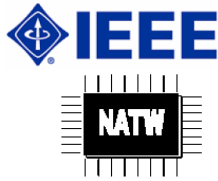
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**Call for Participation**

**21<sup>st</sup> North Atlantic Test Workshop**  
**May 9-11, 2012**  
**Woburn, MA (Greater Boston Area)**

Sponsored by the **IEEE Boston Section**, and in  
Cooperation with **tttc**  
and the **IEEE Green Mountain Section**

**2012 NATW web site:** <http://www.eng.auburn.edu/~strouce/NATW2012.html>

The IEEE North Atlantic Test Workshop provides a forum for discussions on the latest issues relating to high quality, economical, and efficient test methodologies and designs. In addition to traditional topics, the 21<sup>st</sup> NATW will feature test in 3D and die stacking with a theme of “*Test Challenges in Mobile Computing.*” Topics are not limited to, but can include the following:

- |   |                                   |
|---|-----------------------------------|
| Analog, Mixed Signal & RF Testing           | Memory & MEMS Testing             |
| Board Level Testing                         | Nanotechnology Testing            |
| Delay & Performance Testing                 | System-on-Chip (SOC) Test & Debug |
| Design Verification/Validation              | Test Quality/System Reliability   |
| DFM, Defect Analysis & Defect-Based Testing | Test Resource Partitioning        |

**Jake Karrfalt Best Student Paper Award**

To encourage student participation in the testing research community, NATW has sessions dedicated to student presentations and includes a Best Student Paper Award.

**James Monzel Service Award:** This award was inaugurated in 2011 in memory of James Monzel, a long-time NATW organizer with distinguished records of service to NATW and IEEE communities. The award is issued to individuals for dedicated service to NATW. The 2011 recipient is Edmond S. Cooley for his 20 years of dedicated service to NATW.

**Program Highlights**

**Tutorial:** “Testing of 3-D Stacking Devices”

Speaker: Prof. Krishnendu Chakrabarty, Duke University  
(Tutorial is **free** to all registered NATW-2012 attendees)

**Panel:** “Test Challenges of 3D-Stacking Structures”

Panel Chair: Eugene Atwood, IBM     Organizer: Yu Huang, Mentor Graphics

**Keynote:** “Opportunities and Challenges in the Design and Test of Post-CMOS Memories” by Dr. Fabrizio Lombardi, Northeastern University

**Invited Address:** “Data Analytics and Applications for End-to-End Optimization” by Matthias Kamm, Cisco

**Informational Session:** Women in Engineering (WIE)

**Friday Keynote:** “Timing Tests in the Face of Random Transistor Performance Variability” by Prof. Adit Singh, Auburn University

**For general information, contact:**

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Corporate/Academic Support packages available for 2012. Please see our website for more details. Corporate/Academic Supporters for 2011 included:

