Project Debugging with the µVision Debugger

Notes:

- This document assumes ARM Keil MDK (*μVision IDE Version 5.xx*) is installed with the required ST-Link USB driver and device family pack for the target board:
 - STM32F4xx_DFP for STM32F411E-Discovery board STM32F3xx_DFP for STM32F3348-Discovery board
- It is also assumed that a project has been created and successfully built with the μVision IDE, and a debug session initiated to download the project, as described in the document STM32 Discovery Board Projects.

The MDK-ARM debug window (Figure 1) is the same whether debugging in the target hardware or the simulator. The debugger allows you to run/stop/step the program, use breakpoints, and to monitor selected program elements and microcontroller resources.

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Register	Value	46: int main(void) { 47: unsigned char sw 48: unsigned char la	1;	//state of Si	Disass	embly î
R0 R1 R2	0x20000080 0x20000280 0x20000280	49: 0x08000260 4770 BX	lr	//state of h	EDI	
R3 	0x20000280 0x00000000	50: PinSetup(); 0x08000262 F7FFFAD BL.W 51: led1 = 0;	PinSetup (0	//Confic x080001C0) //Initial LEI	gure GPIO pins D state	-
R5 	0x0000000 0x00000000 0x00000000	Lab1.c startup_stm32l1xx_	ndp.s			• • ×
R8 	0x0000000 0x0000000 0x080005D8	48 unsigned char le	d1;	//state of LED1		*
R11 R12 B13 (SP)	0x00000000 0x20000060 0x20000680	51 led1 = 0; 52		//Configure GPIO p //Initial LED state	e So	urce
	0x080001AF 0x08000262 0x21000000	53 /* Endless loop 54 ⊡ while (1) { 55 if (led1 == 0)	*/	//Can also use: fo //LED off?	or(;;) {	
Banked System	ARM	56 GPIOC->BSRRL 57 else 58 GPIOC->BSRRH	= 0x0100; = 0x0100;	<pre>//Reset PC8=0 to to //LED on //Set PC8=1 to turn</pre>	urn off blue LED n on blue LED	E
	Registers	59 sw1 = GPIOA->I 60	DR & 0x01;	//Read GPIOA and is	solate bit O	-
🔚 Project 🧱 Re	gisters			//walls 100	00. 850	F
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ASSIGN Break	Disable BreakEnable	BreakKill BreakList BreakSet	🔓 Call Stack + Lo	cals 🐺 Trace Exceptions 🐺	Event Counters 🛄 Mem	ory 1
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Figure 1. MDK uVision debug window.



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- Click the *Debug* icon in the μ*Vision* IDE or select *Debug* → *Start/Stop Debug Session* to download the project code to the target board (or simulator), program it into the microcontroller's flash memory, and open the Debug Window.
- Click on the **Debug** icon in the Debug window to stop the debug session and return to the *uVision* IDE.
- Upon exiting a debug session, all Debug window settings will automatically be saved. These settings will be restored the next time you initiate a debug session for this project.

Source and Disassembly Windows

The **Source** window, in the center of the debug window (see Figure 1), displays the program code in selected project files, in the language used in each file (C, assembly, etc.) Clicking on a tab at the top of the window displays the source code in that file. If you wish to view a source file that does not have a tab in this window, you may add it from the menu bar by selecting **File > Open**, and then selecting the desired file.

The **Disassembly** window, immediately above the Source window, displays the disassembled code corresponding to the file currently displayed in the Source window. If the source is a C file, the disassembled code shows the assembly language generated for each C statement by the C compiler, including the hexadecimal memory address and object code for each assembly language statement.

In both windows, a yellow arrow or marker points to the next instruction to be executed. A blue marker in the Source window indicates a "cursor" position, which can be used during debugging. Shaded boxes at the left edge of these panes may be used to insert and remove breakpoints, as described below.

Notes:

- If the arrow in the disassembly window does not point to one of your program instructions, the program counter (PC) register has not been set to the first address of your program in memory. This must be changed with a debug command (see Section 8 of the *STM32 Discovery Board Projects* document) or by double-clicking on the PC register in the ARM Registers pane and changing the value to the correct address.
- The shaded boxes at the left edge of the Source pane indicate executable instructions. If there is no shaded box where you want to set a breakpoint, then that instruction did not assemble correctly and there is no corresponding instruction code in memory.

Running the program

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A program may be executed one instruction at a time, executed up to a breakpoint, or simply executed without halting. Execution of the program is controlled via the icons in the left portion of the tool bar, immediately above the Register window. (These operations can also be executed from the Debug menu in the menu bar or from a pop-up menu produced by right-clicking in a debug window.) From left to right, the icon functions are as follows.



- **Reset** reset the CPU and wait for the program to be started.
- Run begin executing the program and continue until some stop condition is reached, such as a breakpoint or error condition
- Stop stop execution of the program, with all panes indicating the state of the program and CPU at the time the stop took effect
- Step execute a single instruction of the current program (C or assembly)
- Step Over similar to Step, but execute any function/subroutine as a single "step"
- Step Out executed instructions until the current function/subroutine is exited
- Run to Cursor you may click on any instruction to set the position of a cursor (indicated by a blue marker at the left edge of the Source pane), and then execute instructions until that instruction is reached (at this point, the blue and yellow markers will coincide.)

Breakpoints

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A **breakpoint** is a designated instruction at which program execution should stop (breaking the flow of the program). Breakpoints are useful debugging tools

- Determine whether the program reaches that point in the program. If the program never stops, then one knows that the instruction at the breakpoint was never executed.
- Stop the program to examine and/or change program variables, memory, or microcontroller registers at that point in the program.
- Stop the program to allow one to step through instructions individually to investigate the flow of the program one instruction at a time.

A breakpoint can be set by clicking in the shaded box at the left edge of the Source or Disassembly pane, next to the instruction at which the program should stop. A red dot will appear to mark the location of the breakpoint. Clicking on a red dot will remove the breakpoint. (As indicated above, if there is no shaded box next to an instruction, then that instruction did not assemble and there is no instruction code in memory.)

One can also set and remove breakpoints via the Debug menu in the menu bar, by right-clicking in the Source or Disassembly pane to produce a pop-up menu, or by clicking on breakpoint icons in the top left tool bar of the debug window. Options available are:

- Insert/remove breakpoint at the current cursor position.
- Kill all breakpoints in a program. This may be more convenient than removing breakpoints one at a time.
- Enable/disable breakpoint at the current cursor position. A defined breakpoint can be "disabled" to prevent the program from stopping at that instruction, and then subsequently "enabled" to allow the program to break at that instruction.



Monitoring program variables and system resources

There are several debug windows that display useful information during program execution, including program variables, CPU registers, system memory, and various microcontroller peripheral function registers. These windows will update dynamically during execution of a program, so that one may determine the state of the program and whether the program is executing as expected.

Register Window (left side of the debug window) - displays the current contents of the CPU registers.

Call Stack + Locals (bottom right corner of the debug window) – displays the current stack contents, including:

- The names of the main program and any "called" functions. For example, if the "main" program calls function X, which then calls function Y, then all three functions will be listed in this window, including return addresses and any local variables within each function.
- Local variables within the currently-executing functions. Local variables are allocated memory on the stack when a function is entered, and then removed from the stack when the function returns to the calling program. You may right-click on any variable to change the display format, for example hexadecimal vs. decimal format.

	Call Stack + Locals						џ	
*	Name	Location/Value	Туре					
	🖃 🔍 🔶 delay	0x08000248	void f	0				
	🔗 i	0x0000A009	auto -	int	1			
	🧼 🧼 j	0x40020818	auto -	· int	1			
	🗄 🔍 💊 main	0x08000292	int f()					
Ŧ	🔷 🔗 sw1	0x01	auto -	unsigned char				
	🔷 led1	0x00	auto -	unsigned char				
	🖧 Call Stack + Locals	Watch 1 🛛 🛄 Memory 1						
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Memory Window (bottom left corner of the debug window) – Up to four memory windows can be opened, each displaying the contents of selected addresses in memory. As shown in Figure 3(a), the starting address of a block of memory to be displayed is entered in the Address box, remembering that hexadecimal values begin with "0x" (otherwise the address will be interpreted as a decimal value.) The data format can be changed as desired, to facilitate studying the data, by right-clicking in the window and selecting the desired format:



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- Unsigned or Signed number format.
- For each format, signed or unsigned, you can select "char", "short", "int", or "long", to display data as one, two, four, or eight-byte values, respectively. Figure 3 format is Unsigned > int.
- Decimal change the displayed data from hexadecimal to decimal format.

See different example formats in Figures 3(b)-3(d). You should select a format that best facilitates studying the information displayed in the window. If necessary, you may open multiple memory windows with a different format for each.

							,	
3	Memory 1						ť	r 💌
A.	Address: 0x2000	00000						
	0x20000000:	000000C	01E84800	08060403	2018100C	00000030	00000000	
	0x20000018:	03020100	08070604	00000009	00000000	00000000	00000000	
	0x20000030:	00000000	00000000	00000000	00000000	00000000	00000000	
Ψ.	0x20000048:	00000000	00000000	00000000	00000000	00000000	00000000	
	0x20000060:	00000000	00000000	00000000	00000000	00000000	00000000	
	0x20000078:	20000024	080001A5	00000000	00000000	00000000	00000000	-
	Call Stack + L	ocals Watch	1 Memor	y 1				
	ST-Link Deb	ugger	t1: 54.3090	0140 sec	L:40 C:1	CAP NUM	SCRL OVR R/V	N ad

Figure 3(a). Memory 1 Window, showing the contents of memory beginning at address 0x20000000, displayed as 32-bit unsigned integer values.

· •	Memory I																												4	*
^	Address: 0x2000	00000	V	arA		_	Va	rВ	_			ι	Jns	ign	ed	Cha	ar F	orr	nat	:										^
	0x20000000:	02	00	00	00	FB	FF	FF	FF	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
	0x2000001C:	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
× .	0x20000038:	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
>	0x20000054:	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	
	0x20000070:	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	\sim
	Call Stack + L	ocals	W	atch	1	M	emor	y 1																						
							Sim	ulati	on						t1: (0.000	0000	0 sec		L:1	90 C:	1		CAI	NU	M S	CRL	OVR	R/W	

Figure 3(b). Memory 1 Window, showing values in	n "Unsigned Char" (8-bit byte) format.
---	--

I	Memory 1										д X
	Address: 0x2000	₀ VarA	VarB		U	nsigned H	ex Int Fori	nat			<u>^</u>
	0x20000000:	00000002	FFFFFFB	00000000	0000000	00000000	00000000	00000000	00000000	00000000	
	0x20000024:	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	
	0x20000048:	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	
	0x2000006C:	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	
ľ	0x20000090:	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	× .
	Call Stack + L	ocals Watch	1 Memor	y 1							
			Sim	ulation		t1: 0.000	00000 sec	L:190 C:1	CAP NU	M SCRL OVR	R/W:

Figure 3(c). Memory 1 Window, showing values in "Unsigned Hex Int" (32-bit word) format.

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memory i											+	^
Address: 0x20000	VarA	VarB	S	igneo	d Decimal Int	Forma	at					^
0x20000000:	0000000002	-0000000005	0000000	0000	0000000000	000000	00000	00000000	000	000000	00000	
0x2000001C:	0000000000	0000000000	0000000	0000	0000000000	000000	00000	00000000	000	000000	00000	
0x20000038:	0000000000	0000000000	0000000	0000	0000000000	000000	00000	00000000	000	000000	00000	
0x20000054:	0000000000	0000000000	0000000	0000	0000000000	000000	00000	00000000	000	000000	00000	
0x20000070:	0000000000	0000000000	0000000	0000	0000000000	000000	00000	00000000	000	000000	00000	Υ.
Call Stack + Lo	cals Watch 1	Memory 1										
		Simulation			t1: 0.0000000	sec	L:190 C:1	CA	NUM	SCRL O	VR R/W	

Figure 3(d). Memory 1 Window, showing values in "Signed Decimal Int" (32-bit word) format.

Watch Window (bottom left corner of the debug window) – displays values of selected program variables and resources. This enables one to monitor the key program variables to determine the current state of the program, and whether the program is producing expected results. One or two Watch windows can be created during a debug session. To display a variable in a watch window, a variable must be "global" and not "local". For assembly language, refer to the EXPORT directive described below.

_							,	
	Watch 1						Ļ	
^	Name	Value	Туре					
	🔷 toggles	0x000000C	int					
	🔷 🔗 sw1	<cannot evaluate=""></cannot>	uchar					
	<enter expression=""></enter>							
Ψ.								
	Call Stack + Locals	Watch 1 Memory 1						
	ST-Link Debugger	t1: 54.3090014	D sec	L:50 C:18	CAP NUM	SCRL OVR	R/W	

Figure 4. Watch 1 Window, showing the current value of global variable "toggles" and main program local variable "sw1" (undefined at the moment, since the program is executing another function.)

To add a variable, for example "toggles", to window Watch 1, locate variable "toggles" in any statement in the Source Window, right click on it, and select: *Add 'toggles' to Watch 1*. The variable will be displayed, as in Figure 4, with its current value and data type. You can change the format of the displayed value between hexadecimal and decimal by right-clicking on the variable name in the Watch window and selecting the desired format. The variable can be removed from a Watch window by right-clicking on the variable name in the Watch window and selecting *Remove Watch 'toggles'*.

The values displayed in a Watch window change dynamically as the program executes, for variables that are "within scope". Referring to Figure 4, variables "within scope" include any global variables in the program, such as variable "toggles", and any local variables within the currently-executing function. In Figure 4, variable "sw1" is defined in a different function from the one that is currently executing, and thus the value is listed as "cannot evaluate".



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Assembly language: To display data in a watch window (as in Figure 4), corresponding to memory labels in a data area, the labels must be "exported" to make them global. For example:

	EXPORT	T toggles	;export label "toggles" to make it "global"
	EXPORT	۲sw1	;export label "sw1" to make it "global"
toggles	dcd	value	;32-bit data
sw1	dcb	value	;8-bit data

You can also change the current value of a variable by double-clicking on the value displayed in the Watch window and entering the desired value. This might be useful if you need to test a part of a program that is executed only for a particular value of some variable.

Other Debug Windows: can be opened from the View menu in the Debug Window menu bar.

Logic Analyzer Window (Not supported by the STM32F3348-Discovery board): graphically displays values of selected global variables over time. The procedure for setting up the logic anlyzer is as follows:

- 1. Configure Serial Wire Viewer (SWV):
 - a. Select **Target Options > Debug tab > Settings**. On the right side of this window. Confirm SW is selected. SW selection is mandatory for SWV. ST-Link uses only SWD.
 - b. Select the Trace tab, shown in Figure 5. Select *Trace Enable*. Unselect *Periodic* and *EXCTRC*. Set *Core Clock* to the frequency that you have set for the core of your microcontroller. (168 MHz was used for the project shown in Figure 5.) Click OK to return to the Debug tab.

Cortex-M Target Driver Setup	×
Debug Trace Flash Download	
Core Clock: 168.000000 MHz	I Trace Enable
Trace Port Serial Wire Output - UART/NRZ SWO Clock Prescaler: 84 ✓ Autodetect SWO Clock: 2.000000 MHz	Timestamps Trace Events Image: Prescaler: Image: Prescaler: PC Sampling EXC: Exception overhead Image: Prescaler: Image: Prescaler: Image: Prescaler: Image: Prescaler: <t< td=""></t<>
ITM Stimulus Ports Enable: 0xFFFFFFF Drivitors 0.00000000	Port 24 23 Port 16 15 Port 8 7 Port 0 Port <
Advanced settings	S124 Port 2316 Port 158 Port 70 YNC OK Cancel Apply

Figure 5. Target options to configure the logic analyzer tool.



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 Create a debug initialization file (ex. *STM32_SWO.ini*) containing the following lines or add them to an existing debug initialization file. Enter that file name in the Debug tab as shown in Figure 6. This configures the STM32 SWV module and default is for SWV. Click OK to return to the main menu.

```
FUNC void DebugSetup (void) {
// <h> Debug MCU Configuration

    > Debug files configuration
    <ol.0> DBG_SLEEP <i> Debug Sleep Mode
    <ol.1> DBG_STOP <i> Debug Stop Mode
    <ol.2> DBG_STANDBY <i> Debug Standby Mode
    <ol.5> TRACE_IOEN <i> Trace I/O Enable
    <ol.6..7> TRACE_MODE <i> Trace Mode

11
11
11
11
11
11
                     <0=> Asynchronous
11
                     <1=> Synchronous: TRACEDATA Size 1
11
                     <2=> Synchronous: TRACEDATA Size 2
11
                    <3=> Synchronous: TRACEDATA Size 4
     <ol.8> DBG_IWDG_STOP <i> Independant Watchdog Stopped when Core is halted
<ol.9> DBG_WWDG_STOP <i> Window Watchdog Stopped when Core is halted
<ol.10> DBG_TIM1_STOP <i> Timer 1 Stopped when Core is halted
11
11
11
     <ol.11> DBG_TIM2_STOP <i> Timer 2 Stopped when Core is halted
11
11
     <ol.12> DBG TIM3 STOP <i> Timer 3 Stopped when Core is halted
11
     <ol.13> DBG TIM4 STOP <i> Timer 4 Stopped when Core is halted
11
    <ol.14> DBG CAN STOP <i> CAN Stopped when Core is halted
// </h>
    WDWORD(0xE0042004, 0x00004027); // DBGMCU CR
   }
```

Options for Target 'Target 1'	×
Device Target Output Listing User C/C++ Asm	Linker Debug Utilities
C Use Simulator with restrictions Settings Limit Speed to Real-Time	
Load Application at Startup Initialization File: Edit	Load Application at Startup Run to main() Inualization File. Edit Edit
Restore Debug Session Settings Breakpoints Toolbox Watch Windows & Performance Analyzer Memory Display System Viewer	Restore Debug Session Settings I reakpoints I Toolbox I watch Windows I Memory Display I System Viewer
CPU DLL: Parameter: SARMCM3.DLL -REMAP -MPU Dialog DLL: Parameter:	Driver DLL: Parameter: SARMCM3.DLL -MPU
DCM.DLL PCM4	TCM.DLL pCM4
OK Car	Icel Defaults Help

Figure 6. Debug initialization file configures SWV for the logic analyzer.



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- 3. To open and configure the logic analyzer, within the Debugger open View/Analysis Windows and select *Logic Analyzer*, or select the LA window on the toolbar. The LA can also be configured while the program is running.
- 4. In your source file, right click on a variable to be displayed (ex. **value)** and select *Add value to*... and then select *Logic Analyzer*. You can also Drag and Drop or enter manually. Note that this should be a "global variable", so that it always has a value.
- 5. Click on the Select box and the LA Setup window appears (Figure 7). With **value** selected, set Display Range Max: to 0x15 as shown in Figure 7, and then click on Close.

Setup Logic Analyzer	X
Current Logic Analyzer Signals:	*
value	
•	Þ
Signal Display	Display Range
Display Type: Analog 💌	Max: 0xFFF
Color:	Min: 0x0
Hexadecimal Display	
Display Formula (Signal & Mask) >>	Shift
And Mask: 0xFFFFFFF	Shift Right: 0
Export / Import	
Export Signal Definitions	Import Signal Definitions
Kill All	Close Help

Figure 7. Logic Analyzer setup window – configure variable display range.

6. Click on Run to run the program. In the Logic Analyzer window (Figure 8), click on Zoom Out until Grid is about 1 second. The variable value will increment to 0x10 (decimal 16) and then is set to 0.

TIP: You can show up to 4 variables in the Logic Analyzer. These variables must be global, static or raw addresses such as *((unsigned long *)0x20000000).

- 7. Enter the static variable **btns** into the LA and set the Display Range Max: to 0x2. Click on RUN and press the User button and see the voltages in Figure 8.
- 8. Select *Signal Info, Show Cycles, Amplitude* and *Cursor* to see the effects they have.

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Figure 8. Logic Analyzer showing two variables.





Example

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Global variables **phasea** through **phased** of Figure 10 are toggled between 0 and 1 at different rates by tasks of a "blinky" program.

1. Add the four variables to the Logic Analyzer window. These variables will be listed on the left side of the LA window as shown in Figure 11.

Note: The Logic Analyzer can display static and global variables, structures and arrays. It can't see locals: just make them static. To see peripheral registers read or write to them and enter them in the LA. Note that you can view signals that exist mathematically in a variable and not available for measuring in the outside world.



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Figure 10. Four global variables to be monitored in the Logic Analyzer.

- 2. Adjust the scaling according to maximum/minimum values to be displayed. Click on the LA *Setup* icon and click on each of the four variables and set Max. in the Display Range: to 0x3. (In this application, variables toggle between 0 and 1.)
- 3. Use the *All*, *OUT* and *In* buttons set the range to 1 second or so. Move the scrolling bar to the far right if needed.
- 4. As shown in Figure 11, select *Signal Info* and *Show Cycles*. Click to mark a place and move the cursor to get timings. Place the cursor on one of the waveforms and get timing and other information as shown in the inserted box labeled *phasec*.



Figure 11. Logic Analyzer measurements of signal *phasec*.



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