

ELEC 5260_6260 Homework #12
Due Wednesday, 4/21/2010

Solve the following problems from the end of Chapter 6 (Processes and Operating Systems)

- 6-1 – Discussion of process rates
- 6-4 – Class diagram for a process
- 6-8 and 6-9 (related) – Schedules and CPU utilization
- 6-11 – Discussion of blocking/non-blocking communication
- 6-12 – Program P() and V() functions
- 6-13 – UML sequence diagram for atomic/non-atomic T&S
- 6-15 – RMS scheduling
- 6-16 – RMS scheduling
- 6-20 – Compare RMS/EDF scheduling
- 6-21 – Scheduling with data dependencies
- 6-24 – Stored memory vs. message passing selection