ELEC 4200 Review – Textbook Chapters

(In addition to posted presentations and labs on the course web page)

Digital Logic Review:

• Chapter 1: all
• Chapter 5.1-5.4: state machines

VHDL Topics

• Chapter 2 (fundamentals): all
• Chapter 4 (design examples): Sections 4.1 – 4.9
• Chapter 8 (adv. Topics): Sections 8.3 – 8.6, and 8.9-8.12

PLDs and FPGAs

• Chapter 3 (basic PLD,FPGA structures): all
• Chapter 6 (design with FPGAs): all

Boundary Scan

• Chapter 10 (boundary scan): Sections 10.3-10.4

Verilog

• Overview slides and “mini-tutorial” on course web page

Additional reference for VHDL, Verilog and FPGAs:

Application-Specific Integrated Circuits, by Michael John Sebastian Smith (on-line)