

Researches

Dr. Foster Dai's research at Auburn is focused on integrated circuit designs for wireless, wire-line, radar and satellite applications. At Auburn, Dr. Dai has developed an RFIC program with over 10 graduate students, a state-of-art RFIC testing lab, an access to first-class semiconductor fabrications such as IBM's SiGe and CMOS technologies and multi-million dollar research funding sponsored by various government and industrial funding agents.

Prospective students who are interested in integrated circuit designs are encouraged to contact Dr. Dai by emails (daiifa01@auburn.edu) for MS and PhD studies with possible financial support in form of graduate research assistantship (GRA). Experience, skills and education required for GRA positions include

- BSEE with excellent GPAs
- Strong fundamentals in electronics, circuits, microwave techniques, and communications
- Experience in integrated circuit designs
- Familiarity with CAD tools such as Cadence, ADS, Mentor and Matlab
- Strength in documentation and presentation

For admission to Graduate School of Auburn University, please visit <http://www.grad.auburn.edu/>.