In Memoriam

Nagarajan “Ranga” Ranganathan

30 March 1961 - 25 October 2018
Our long-time associate Nagarajan Ranganathan (known to us as Ranga) passed away in Tampa, Florida, USA on October 25, 2018. Wife Radhika, daughter Sheela and son Shiva survive him. *We will miss you, Ranga.*

I first met Ranga in January of 1991 on a flight to USA after attending the *Fourth International Conference on VLSI Design* in New Delhi. He was returning to Tampa where he taught at the University of South Florida (USF). From that day onward, he maintained a close association with the Conference, playing key roles such as those of general chair and program chair. He served on the Steering Committee from 1992. *We feel Ranga’s absence.*

The Conference, VLSID as we call it, and Ranga were a perfect match. Everything about Ranga was “VLSI”. He graduated from the National Institute of Technology, Tiruchirapalli, India and obtained a PhD from the University of Central Florida, Orlando, USA. In 1988, He joined USF, finally rising to the position of Distinguished University Professor in the Department of Computer Science and Engineering. His areas of research were VLSI circuits and systems design, VLSI design automation, computer architecture, low power design, reversible logic, nano-architectures, hardware algorithms, parallel processing, data compression, and VLSI for vision, video, image processing and pattern recognition. He wrote four books, held ten patents and published over 300 papers. He received a large number of awards, medals and recognitions. He was a member of the VLSI Society of India. In 2002, he was elected a Fellow of the IEEE for contributions to algorithms and architectures for VLSI systems design. He was Editor-In-Chief (2003-2006) of *IEEE Transactions on VLSI Systems*. He chaired the IEEE Computer Society’s Technical Committee on VLSI (TCVLSI). His professional contributions are very significant, and too numerous to list here. *The field of VLSI will remember Ranga for years to come.*