

Neuro-fuzzy Systems and Their Applications

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II. NEURON

Biological neuron has a complicated structure which receives trains of pulses on hundreds of *excitatory* and *inhibitory* inputs. Those incoming pulses are summed and averaged with different weights during the time period of *latent summation*. If the summed value is higher than a threshold then the neuron generates a pulse which is sent to neighboring neurons. If the value of the summed weighted inputs is higher, the neuron generates pulses more frequently.

An above simplified description of the neuron action leads to a very complex neuron model which is not practical. McCulloch and Pitts in 1943 show that even with a very simple neuron model it is possible to build logic and memory circuits. The McCulloch-Pitts neuron model assumes that incoming and outgoing signals may have only binary values 0 and 1. If incoming signals summed through positive or negative weights have a value larger than threshold T, then the neuron output is set to 1. Otherwise it is set to 0.

Examples of McCulloch-Pitts neurons realizing *OR*, *AND*, *NOT* and *MEMORY* operations are shown in Fig. 1. Note, that the structure of *OR* and *AND* gates can be identical and only threshold is different. These simple neurons, known also as perceptrons, are usually more powerful than typical logic gates used in computers.

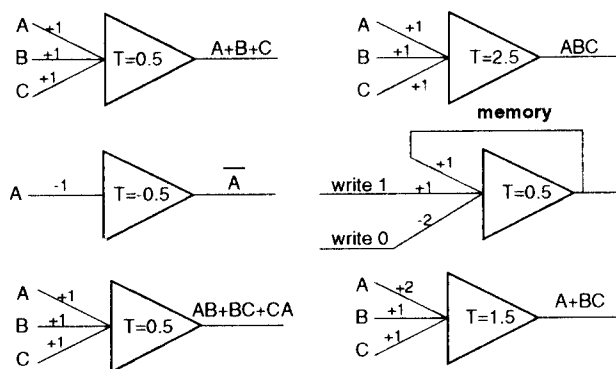


Fig. 1 Several logical operations using networks with McCulloch-Pitts neurons.

Multilayer neural networks usually use continuous activation functions, either unipolar

$$o = f(net) = \frac{1}{1 + \exp(-\lambda net)}$$

or bipolar

$$o = f(net) = \tanh(0.5\lambda net) = \frac{2}{1 + \exp(-\lambda net)} - 1$$

I. INTRODUCTION

Fascination about artificial neural networks started when McCulloch and Pitts in 1943 developed their model of an elementary computing neuron and when Hebb in 1949 introduced his learning rules. A decade latter Rosenblatt introduced the *perceptron* concept. In the early sixties Widrow and Holf developed intelligent systems such as ADALINE and MADALINE. Nilson in his book "Learning Machines" [10] summarized many developments of that time. The publication of the Mynsky and Paper in 1969 wrote the book with some discouraging results and this stopped for sometime a fascination of artificial neural networks, and achievements in the mathematical foundation of the *backpropagation* algorithm by Werbos in 1974 went unnoticed. The current rapid growth of the neural network area started in 1982 with Hopfield recurrent network and Kohonen unsupervised training algorithms. The *backpropagation* algorithm described by Rumelhard in 1986 started rapid development of neural networks.

Abstract - Computational intelligence combines neural networks, fuzzy systems, and evolutionary computing. Neural networks and fuzzy systems, have already proved their usefulness and have been found useful for many practical applications. We are at the beginning of the third technological revolution. Now neural networks are being capable of replacing highly skilled people with all their experience.

The concept of artificial neural networks is presented, underlining their unique features and limitations. A review and comparison of various supervised and unsupervised learning algorithms follows. Several special, easy to train, architectures are shown. The neural network presentation is illustrated with many practical applications such as speaker identification, sound recognition of various equipment as a diagnosis tool, written character recognition, data compression using pulse coupled neural networks, time series prediction, etc.

In the later part of the presentation the concept of fuzzy systems, including the conventional Zadeh approach and Takagi-Sugano architecture, is presented. The basic building blocks of fuzzy systems are discussed. Comparisons of fuzzy and neural systems, are given and illustrated with several applications. The fuzzy system presentation is concluded with a description of the fabricated VLSI fuzzy chip.

These continuous activation functions allow for the gradient based training of multilayer networks. Typical activation functions are shown in Fig. 2.

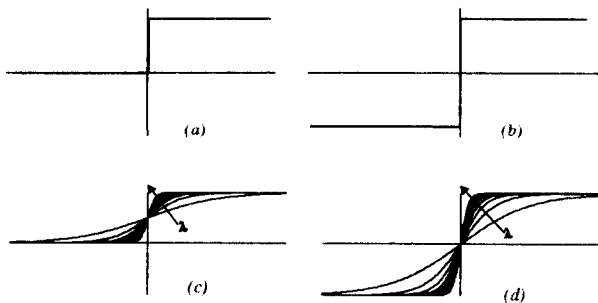


Fig.2. Typical activation functions: (a) hard threshold unipolar, (b) hard threshold bipolar, (c) continuous unipolar, (d) continuous bipolar.

III. FEEDFORWARD NEURAL NETWORKS

Simplest and most commonly used neural networks use only for one directional signal flow. Furthermore, most of feedforward neural networks are organized in layers. An example of the three layer feedforward neural network is shown in Fig. 3. This network consists of input nodes, two *hidden layers*, and an output layer.

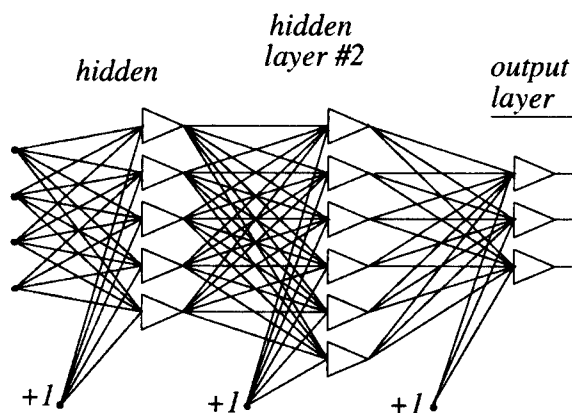


Fig. 3. An example of the three layer feedforward neural network, which is also known as the backpropagation network.

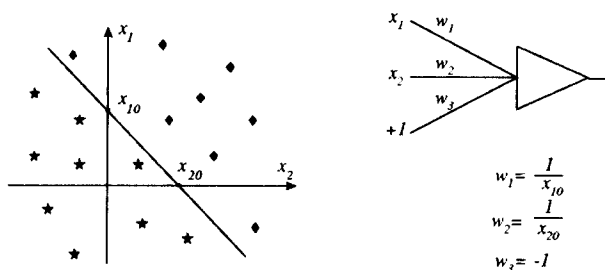


Fig. 4. Linear separation of patterns in the two-dimensional space by a single neuron.

A single neuron is capable of separating input patterns into two categories and this separation is linear. For example, the separation line shown in Fig. 4, which are crossing x_1 and x_2 axes at points x_{10} and x_{20} , can be achieved with a neuron having weights:

$$w_i = \frac{1}{x_{i0}} \quad \text{for } i = 1 \text{ to } n; \quad w_{n+1} = -1$$

One neuron can divide only linearly separated patterns. In order to select just one region in n -dimensional input space more than $n+1$ neurons should be used. If more input clusters should be selected then the number of neurons in the input (hidden) layer should be properly multiplied. If the number of neurons in the input (hidden) layer is not limited, then all classification problems can be solved using the three layer network. The linear separation property of neurons makes some problems specially difficult for neural networks, such as exclusive *OR*, parity computation for several bits, or separate patterns laying on two neighboring spirals.

The feedforward neural networks are used for nonlinear transformation (mapping) of a multidimensional input variable into another multidimensional output variable. In theory, any input-output mapping should be possible if neural network has enough neurons in hidden layers (size of output layer is set by the number of outputs required). Practically, it is not an easy task and presently, there is no satisfactory method to define how many neurons should be used in hidden layers. Usually this is found by try and error method. In general, it is known that if more neurons are used, more complicated shapes can be mapped. On the other side networks with large number of neurons lose their ability for generalization, and it is more likely that such network will try to map noise supplied to the input also.

IV. LEARNING ALGORITHMS

Weights in artificial neurons are adjusted during a training procedure. Various learning algorithms were developed but only a few are suitable for multilayer neuron networks. Some use only local information about signals in the neurons others require information from outputs. Supervised algorithms require a supervisor who always knows what outputs should be unsupervised algorithms need no such information. Common learning rules are described below.

A. Hebbian Learning Rule

Hebb in 1949 developed unsupervised learning rule which was based on the assumption that if two neighbor neurons must be activated and deactivated at the same time, then the weight connecting these neurons should increase. For neurons operating in the opposite phase, the weight between them should decrease. If there is no correlation, the weight should remain unchanged. This assumption can be described by the formula

$$\Delta w_{ij} = c x_i o_j$$

where w_{ij} is the weight from i -th to j -th neuron, c is the learning constant, x_i is the signal on the i -th input and o_j is the output signal. The training process starts usually with values of all weights set to zero. This learning rule can be used for both soft and hard threshold neurons. The absolute values of the weights are usually proportional to the learning time, which is undesired.

B. Correlation learning rule

The correlation learning rule uses a similar principle as the Hebbian learning rule. It assumes that weights between simultaneously responding neurons should be largely positive, and weights between neurons with opposite reaction should be largely negative. Mathematically, this can be written that weights should be proportional to the product of states of connected neurons. In contrary to the Hebbian rule, the correlation rule is of the supervised type. Instead of actual response, the desired response is used for weight change calculation

$$\Delta w_{ij} = c x_i d_j$$

This training algorithm starts with initialization of weights to zero values.

C. Instar learning rule

If input vectors, and weights, are normalized, or they have only binary bipolar values (-1 or $+1$), then the *net* value will have the largest positive value when the weights have the same values as the input signals. Therefore, weights should be changed only if they are different from the signals

$$\Delta w_i = c(x_i - w_i)$$

Note, that the information required for the weight is only taken from the input signals. This is a local and unsupervised learning algorithm.

D. WTA - Winner Takes All

The WTA is a modification of the instar algorithm where weights are modified only for the neuron with the highest *net* value. Weights of remaining neurons are left unchanged. This unsupervised algorithm (because we do not know what are desired outputs) has a global character. The WTA algorithm, developed by Kohonen in 1982, is often used for automatic clustering and for extracting statistical properties of input data.

E. Outstar learning rule

In the outstar learning rule it is required that weights connected to the certain node should be equal to the desired outputs for the neurons connected through those weights

$$\Delta w_{ij} = c(d_j - w_{ij})$$

where d_j is the desired neuron output and c is small learning constant which further decreases during the learning procedure. This is the supervised training procedure because desired outputs must be known. Both instar and outstar learning rules were developed by Grossberg in 1974.

F. Widrow-Hoff (LMS) learning rule

Widrow and Hoff in 1962 developed a supervised training algorithm which allows to train a neuron for the desired response. This rule was derived by minimizing the square of the difference between *net* and output value.

$$Error_j = \sum_{p=1}^P (net_{jp} - d_{jp})^2$$

where $Error_j$ is the error for j -th neuron, P is the number of applied patterns, d_{jp} is the desired output for j -th neuron when p -th pattern is applied. This rule is also known as the LMS (Least Mean Square) rule. By calculating a derivative of the error with respect to w_i , one can find a formula for the weight change.

$$\Delta w_{ij} = c x_{ij} \sum_{p=1}^P (d_{jp} - net_{jp})$$

Note, that weight change Δw_{ij} is a sum of the changes from each of the individual applied patterns. Therefore, it is possible to correct weight after each individual pattern was applied. If the learning constant c is chosen to be small, then both methods gives the same result. The LMS rule works well for all type of activation functions. This rule tries to enforce the *net* value to be equal to desired value. Sometimes, this is not what we are looking for. It is usually not important what the *net* value is, but it is important if the *net* value is positive or negative. For example, a very large *net* value with a proper sign will result in large error and this may be the preferred solution.

G. Linear regression

The LMS learning rule requires hundreds or thousands of iterations before it converges to the proper solution. Using the *linear regression the same result can be obtained in only one step*.

Considering one neuron and using vector notation for a set of the input patterns \mathbf{X} applied through weights \mathbf{w} the vector of *net* values \mathbf{net} is calculated using

$$\mathbf{X}\mathbf{w} = \mathbf{net}$$

where \mathbf{X} is a rectangular array $(n+1)*p$, n is the number of inputs, and p is the number of patterns. Note that the size of the input patterns is always augmented by one, and this additional weight is responsible for the threshold. This method, similar to the LMS rule, assumes a linear activation function, so the *net* values \mathbf{net} should be equal to desired output values \mathbf{d}

$$\mathbf{X}\mathbf{w} = \mathbf{d}$$

Usually $p > n+1$, and the above equation can be solved only in the least mean square error sense

$$\mathbf{w} = (\mathbf{X}^T \mathbf{X})^{-1} \mathbf{X}^T \mathbf{d}$$

or to convert the set of p equations with $n+1$ unknowns to the set of $n+1$ equations with $n+1$ unknowns. Weights are a solution of the equation

$$\mathbf{Y}\mathbf{w} = \mathbf{z}$$

where elements of the \mathbf{Y} matrix and the \mathbf{z} vector are given by

$$y_{ij} = \sum_{p=1}^P x_{ip} x_{jp} \quad z_i = \sum_{p=1}^P x_{ip} d_p$$

The LMS method assumes linear activation function $net = o$, and the obtained solution is sometimes far from optimum as it is shown in Fig. 5 for a simple two dimensional case, with four patterns belonging to two categories. In the solution obtained using the LMS algorithm one pattern is misclassified. If error is defined as

$$Error_j = \sum_{p=1}^P (o_{jp} - d_{jp})^2$$

Then the derivative of the error with respect to the weight w_{ij} is

$$\frac{d Error_j}{d w_{ij}} = 2 \sum_{p=1}^P (o_{jp} - d_{jp}) \frac{df(net_{jp})}{d net_{jp}} x_i$$

Note, that this derivative is proportional to the derivative of the activation function $f(net)$. Thus, this type of approach is possible only for continuous activation functions and this method cannot be used with hard activation functions. In this respect the LMS method is more general. The derivatives most common continuous activation functions are

$$f' = o(1 - o) \text{ for the unipolar and}$$

$$f' = 0.5(1 - o^2) \text{ for the bipolar.}$$

Using the cumulative approach, the neuron weight w_{ij} should be changed with a direction of gradient

$$\Delta w_{ij} = c x_i \sum_{p=1}^P (d_{jp} - o_{jp}) f'_{jp}$$

in case of the incremental training for each applied pattern

$$\Delta w_{ij} = c x_i f'_{jp} (d_{jp} - o_{jp})$$

the weight change should be proportional to input signal x_i , to the difference between desired and actual outputs $d_{jp} - o_{jp}$, and to the derivative of the activation function f'_{jp} . Similar to the LMS rule, weights can be updated in both the incremental and the cumulative methods. In comparison to the LMS rule, the delta rule always leads to a solution close to the optimum. As it is illustrated in Fig 5, when the delta rule is used, all four patterns are classified correctly.

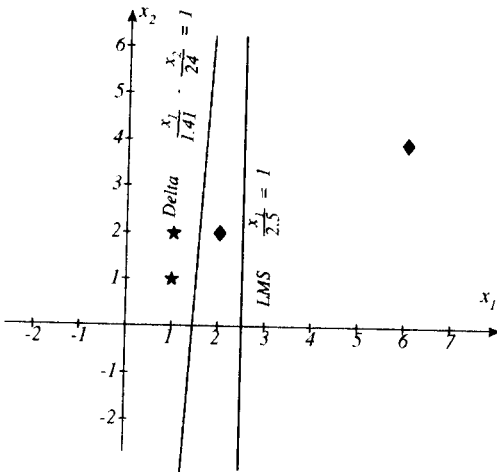


Fig. 5. An example with a comparison of results obtained using LMS and Delta training algorithms. Note that LMS is not able to find the proper solution.

The delta method is usually very slow. Solution can be fast when nonlinear regression algorithm is adopted [1]. The total error for one neuron j and pattern p is now defined as a simple difference:

$$E_{jpo} = d_{jp} - o_{jp}(net)$$

where $net = w_1 x_1 + w_2 x_2 + \dots + w_n x_n$. The derivative of the error with respect to the i^{th} weight of the j^{th} neuron can be written as:

$$\frac{dE_{jpo}}{dw_i} = \frac{do_{jp}}{dnet} \frac{dnet}{dw_i} = -f'_{jp} x_{ijp}$$

The error function can then be approximated by the first two terms of the linear approximation around a given point:

$$E_{jp} = E_{jp0} + \frac{dE_{jp}}{dw_1} \Delta w_1 + \frac{dE_{jp}}{dw_2} \Delta w_2 + \dots + \frac{dE_{jp}}{dw_n} \Delta w_n$$

therefore

$$\begin{bmatrix} x_{11} & x_{12} & x_{13} & \dots & x_{1i} \\ x_{21} & x_{22} & x_{23} & \dots & x_{2i} \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ x_{p1} & x_{p2} & x_{p3} & \dots & x_{pi} \\ \vdots & \vdots & \vdots & \ddots & \vdots \\ x_{P1} & x_{P2} & x_{P3} & \dots & x_{Pi} \end{bmatrix} \begin{bmatrix} \nabla w_1 \\ \nabla w_2 \\ \vdots \\ \nabla w_i \end{bmatrix} = \begin{bmatrix} \frac{d_1 - o_1}{f'_1} \\ \frac{d_2 - o_2}{f'_2} \\ \vdots \\ \frac{d_p - o_p}{f'_p} \\ \vdots \\ \frac{d_P - o_P}{f'_P} \end{bmatrix}$$

or

$$\mathbf{X} \Delta \mathbf{w} = \mathbf{v}$$

Matrix \mathbf{X} is usually rectangular and above equation can be only solved using pseudo inversion technique.

$$\Delta \mathbf{w} = (\mathbf{X}^T \mathbf{X})^{-1} \mathbf{X}^T \mathbf{v}$$

The $(\mathbf{X}^T \mathbf{X})^{-1} \mathbf{X}^T$ matrix is composed of input patterns only and it must be computed only once

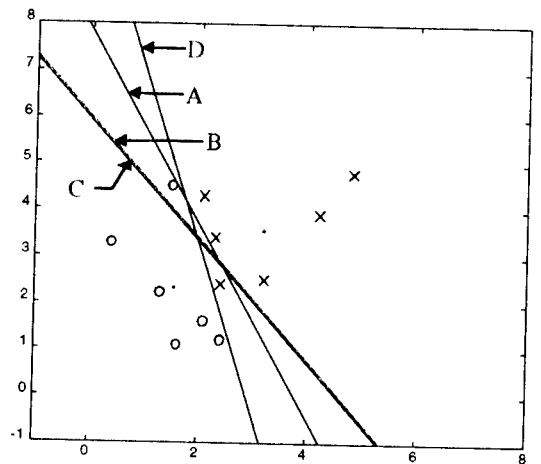


Fig. 6. Comparison of Algorithms where the algorithms can be identified by the labels A-regression, B-minimum distance, C-modified minimum distance, and D-modified regression and delta (back propagation) algorithm.

TABLE 1. Learning rules for single neuron

General formula

$$\Delta w_{ij} = \alpha \delta x$$

Hebb rule (unsupervised):

$$\delta = o$$

correlation rule (supervised):

$$\delta = d$$

perceptron fixed rule:

$$\delta = d - o$$

perceptron adjustable rule:

$$\delta = (d - o) \frac{\mathbf{x}^T \mathbf{w}}{\mathbf{x}^T \mathbf{x}} = (d - o) \frac{net}{\|\mathbf{x}\|^2}$$

LMS (Widrow-Hoff) rule:

$$\delta = d - net$$

delta rule:

$$\delta = (d - o) f'$$

pseudoinverse rule (for linear system):

$$\mathbf{w} = (\mathbf{x}^T \mathbf{x})^{-1} \mathbf{x}^T d$$

iterative pseudoinverse rule (for nonlinear system):

$$\mathbf{w} = (\mathbf{x}^T \mathbf{x})^{-1} \mathbf{x}^T \frac{d - o}{f'}$$

find the total gains as products of layer gains. This procedure is equally or less computational intensive than a calculation of cumulative errors in the error backpropagation algorithm.

The backpropagation algorithm has a tendency for oscillation. In order to smooth up the process, the weights increment Δw_{ij} can be modified according to Rumelhart, Hinton, and Williams [14]

$$w_{ij}(n+1) = w_{ij}(n) + \Delta w_{ij}(n) + \alpha \Delta w_{ij}(n-1)$$

or according to Sejnowski and Rosenberg (1987)

$$w_{ij}(n+1) = w_{ij}(n) + (1 - \alpha) \Delta w_{ij}(n) + \alpha \Delta w_{ij}(n-1)$$

where α is the momentum term.

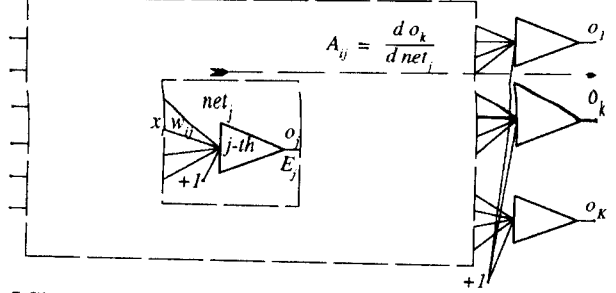


Fig. 7. Illustration of the concept of the gain computation in neural networks

1. Error Backpropagation learning

The delta learning rule can be generalized for multilayer networks. Using a similar approach, as it is described for the delta rule, the gradient of the global error can be computed in respect to each weight in the network. Interestingly

$$\Delta w_{ij} = c x_i f'_j E_j$$

where c is the learning constant, x_i is the signal on the i -th neuron input, and f'_j is the derivative of activation function. The cumulative error E_j on neuron output is given by

$$E_j = \frac{1}{f'_j} \sum_{k=1}^K (o_k - d_k) A_{jk}$$

where K is the number of network outputs, and A_{jk} is the small signal gain from the input of j -th neuron to the k -th network output as Fig. 7 shows. The calculation of the back propagating error is kind of artificial to the real nervous system. Also, the error backpropagation method is not practical from the point of view of hardware realization. Instead, it is simpler to find signal gains from the input of the j -th neuron to each of the network output (Fig. 7). In this case weights are corrected using

$$\Delta w_{ij} = c x_i \sum_{k=1}^K (o_k - d_k) A_{jk}$$

Note, that the above formula is general, no matter if neurons are arranged in layers or not. One way to find gains A_{jk} is to introduce an incremental change on the input of the j -th neuron and observe the change in the k -th network output. This procedure requires only forward signal propagation, and it is easy to implement in a hardware realization. Another possible way, is to calculate gains through each layer and then

The backpropagation algorithm can be significantly speedup, when after finding components of the gradient, weights are modified along the gradient direction until a minimum is reached. This process can be carried on without the necessity of computational intensive gradient calculation at each step. The new gradient components are calculated once a minimum on the direction of the previous gradient is obtained. This process is only possible for cumulative weight adjustment. One method to find a minimum along the gradient direction is the tree step process of finding error for three points along gradient direction and then, using a parabola approximation, jump directly to the minimum. The fast learning algorithm using the above approach was proposed by Fahlman [2] and it is known as the *quickprop*.

The backpropagation algorithm has many disadvantages which leads to very slow convergence. One of the most painful is this, that in the backpropagation algorithm the learning process almost perishes for neurons responding with the maximally wrong answer. For example if the value on the neuron output is close to $+1$ and desired output should be close to -1 , then the neuron gain $f'(net) \approx 0$ and the error signal cannot back propagate, so the learning procedure is not effective. To overcome this difficulty, a modified method for derivative calculation was introduced by Wilamowski and Torvik [24]. The derivative is calculated as the slope of a line connecting the point of the output value with the point of the desired value as shown in Fig. 8

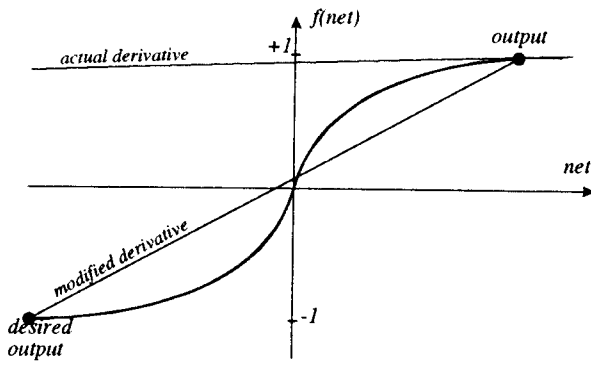


Fig. 8 Illustration of the modified derivative calculation for faster convergence of the error backpropagation algorithm.

$$f_{\text{modif}} = \frac{O_{\text{desired}} - O_{\text{actual}}}{net_{\text{desired}} - net_{\text{actual}}}$$

Note, that for small errors, equation converges to the derivative of activation function at the point of the output value. With an increase of the system dimensionality, a chance for local minima decrease. It is believed that the described above phenomenon, rather than a trapping in local minima, is responsible for convergence problems in the error backpropagation algorithm.

K. Lavenberg-Marquardt learning

The Lavenberg-Marquardt learning algorithm is the second order search method of a minimum. At each iteration step error surface is approximated by parabolic approximation and the minimum of the paraboloid is the solution for the step.

Simples approach require function approximation by first terms of Taylor series

$$F(\mathbf{w}_{k+1}) = F(\mathbf{w}_k + \Delta \mathbf{w}) + \mathbf{g}_k^T \Delta \mathbf{w}_k + \frac{1}{2} \Delta \mathbf{w}_k^T \mathbf{A}_k \Delta \mathbf{w}_k$$

where $\mathbf{g} = \nabla E$ is gradient and $\mathbf{A} = \nabla^2 E$ is Hessian of global error E.

$$\text{Gradient} \Rightarrow \frac{\partial E}{\partial w_1}, \frac{\partial E}{\partial w_2}, \frac{\partial E}{\partial w_3}, \dots$$

$$\text{Hessian} \Rightarrow \begin{matrix} \frac{\partial^2 E}{\partial w_1^2} & \frac{\partial^2 E}{\partial w_1 \partial w_2} & \frac{\partial^2 E}{\partial w_1 \partial w_3} & \dots \\ \frac{\partial^2 E}{\partial w_2 \partial w_1} & \frac{\partial^2 E}{\partial w_2^2} & \frac{\partial^2 E}{\partial w_2 \partial w_3} & \dots \\ \frac{\partial^2 E}{\partial w_3 \partial w_1} & \frac{\partial^2 E}{\partial w_3 \partial w_2} & \frac{\partial^2 E}{\partial w_3^2} & \dots \\ \vdots & \vdots & \vdots & \ddots \end{matrix}$$

Steepest decent (error backpropagation) method calculates weights using:

$$\mathbf{w}_{k+1} = \mathbf{w}_k - \alpha \mathbf{g}$$

while Newton method uses:

$$\mathbf{w}_{k+1} = \mathbf{w}_k - \mathbf{A}_k^{-1} \mathbf{g}$$

The Newton method is practical only for small networks where Hessian \mathbf{A}_k can be calculated and inverted. In the

Lavenberg-Marquardt method the Hessian \mathbf{A}_k is approximated by product of Jacobians

$$\mathbf{A} \approx 2\mathbf{J}^T \mathbf{J}$$

and gradient as

$$\mathbf{g} \approx 2\mathbf{J}^T \mathbf{e}$$

where \mathbf{e} is vector of output errors and Jacobian \mathbf{J} is

$$\text{Jacobian} \Rightarrow \begin{matrix} \frac{\partial E_1}{\partial w_1} & \frac{\partial E_1}{\partial w_2} & \frac{\partial E_1}{\partial w_3} & \dots \\ \frac{\partial E_2}{\partial w_1} & \frac{\partial E_2}{\partial w_2} & \frac{\partial E_2}{\partial w_3} & \dots \\ \frac{\partial E_3}{\partial w_1} & \frac{\partial E_3}{\partial w_2} & \frac{\partial E_3}{\partial w_3} & \dots \\ \vdots & \vdots & \vdots & \ddots \end{matrix}$$

It is much easier to calculate Jacobian than Hessian and also usually Jacobian is much smaller so less memory is required. Therefore weights can be calculated as

$$\mathbf{w}_{k+1} = \mathbf{w}_k - (2\mathbf{J}_k^T \mathbf{J}_k)^{-1} 2\mathbf{J}_k^T \mathbf{e}$$

or

$$\mathbf{w}_{k+1} = \mathbf{w}_k - (\mathbf{J}_k^T \mathbf{J}_k)^{-1} \mathbf{J}_k^T \mathbf{e}$$

To secure convergence the Lavenberg-Marquardt introduces μ parameter

$$\mathbf{w}_{k+1} = \mathbf{w}_k - (\mathbf{J}_k^T \mathbf{J}_k + \mu \mathbf{I})^{-1} \mathbf{J}_k^T \mathbf{e}$$

when $\mu = 0$ this method is similar to the second order Newton method. For larger values of μ parameter the Lavenberg-Marquardt works as the steepest decent method with small time steps. The μ parameter is automatically adjusted during computation process so good convergence is secured. The Lavenberg-Marquardt recently becomes very popular because it will usually converge in 5 to 10 iteration steps. Main disadvantage of this method is a large memory requirement and therefore it cannot be used for

V. SPECIAL FEEDFORWARD NETWORKS

The multilayer backpropagation network, as shown in Fig. 3, is a commonly used feedforward network. This network consists of neurons with the sigmoid type continuous activation function presented in Figures 2(c) and 2(d). In most cases, only the one hidden layer is required, and the number of neurons in the hidden layer are chosen to be proportional to the problem complexity. The number of neurons in the hidden layer is usually found by a try and error process. The training process starts with all weights randomized to small values, and then the error backpropagation algorithm is used to find a solution. When the learning process does not converge, the training is repeated with a new set of randomly chosen weights. Nguyen and Widrow [16] proposed an experimental approach for the two layer network weight initialization. In the second layer, weights are randomly chosen in the range from -0.5 to +0.5. In the first layer, initial weights are calculated from

$$w_{ij} = \frac{\beta z_{ij}}{\|z_j\|}; \quad w_{(n+1)j} = \text{random}(-\beta, +\beta)$$

where z_{ij} is the random number from -0.5 to $+0.5$ and the scaling factor B is given by

$$\beta = 0.7 P_N^{\frac{1}{N}}$$

where n is the number of inputs, and N is the number of hidden neurons in the first layer. This type of weight initialization usually leads to faster solutions.

For adequate solutions with backpropagation networks, many tries are typically required with different network structures and different initial random weights. This encouraged researchers to develop feedforward networks which can be more reliable. Some of those networks are described below.

A. Functional link network

One layer neural networks are relatively easy to train, but these networks can solve only linearly separated problems. The concept of functional link networks was developed by Nilson book [10] and later elaborated by Pao [13] using the functional link network shown in Fig. 9.

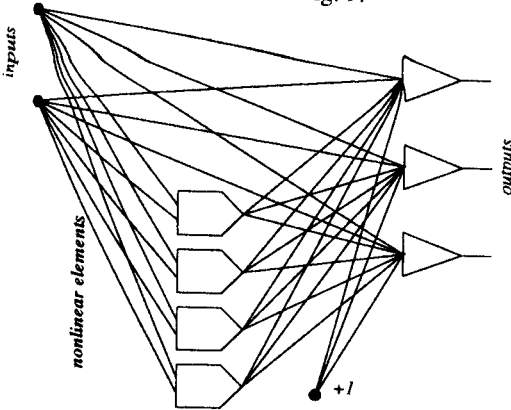


Fig. 9. The functional link network

Using nonlinear terms with initially determined functions, the actual number of inputs supplied to the one layer neural network is increased. In the simplest case nonlinear elements are higher order terms of input patterns. Note that the functional link network can be treated as a one layer network, where additional input data are generated off line using nonlinear transformations. The learning procedure for one layer is easy and fast. Fig. 10 shows an XOR problem solved using functional link networks. Note, that when the functional link approach is used, this difficult problem becomes a trivial one. The problem with the functional link network is that proper selection of nonlinear elements is not an easy task. However, in many practical cases it is not difficult to predict what kind of transformation of input data may linearize the problem, so the functional link approach can be used.

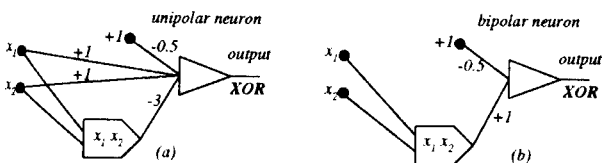


Fig. 10 Functional link networks for solution of the XOR problem: (a) using unipolar signals, (b) using bipolar signals.

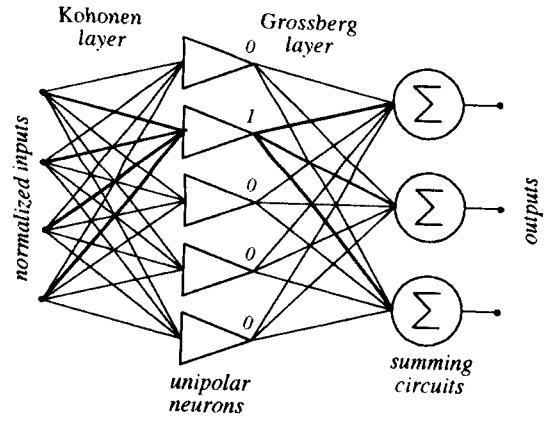


Fig. 11 The counterpropagation network.

B. Feedforward version of the counterpropagation network

The counterpropagation network was originally proposed by Hecht-Nilsen [3] and modified feedforward version described by Zurada [26]. This network, which is shown in Fig. 11, requires numbers of hidden neurons equal to the number of input patterns, or more exactly, to the number of input clusters. The first layer is known as the Kohonen layer with unipolar neurons. In this layer only one neuron, the winner, can be active. The second is the Grossberg outstar layer. The Kohonen layer can be trained in the unsupervised mode, but that need not be the case. When binary input patterns are considered, then the input weights must be exactly equal to the input patterns. In this case

$$net = \mathbf{x}^T \mathbf{w} = (n - 2HD(\mathbf{x}, \mathbf{w}))$$

where n is the number of inputs, \mathbf{w} are weights, \mathbf{x} is the input vector, and $HD(\mathbf{w}, \mathbf{x})$ is the Hamming distance between input pattern and weights.

Since for a given input pattern, only one neuron in the first layer may have the value of one and remaining neurons have zero values, the weights in the output layer are equal to the required output pattern.

The feedforward counterpropagation network may also use analog inputs, but in this case all input data should be normalized

$$\mathbf{w}_i = \hat{\mathbf{x}}_i = \frac{\mathbf{x}_i}{\|\mathbf{x}_i\|}$$

The counterpropagation network is very easy to design. The number of neurons in the hidden layer should be equal to the number of patterns (clusters). The weights in the input layer should be equal to the input patterns and, the weights in the output layer should be equal to the output patterns. This simple network can be used for rapid prototyping.

C. WTA architecture

The winner take all WTA network was proposed by Kohonen in 1988 [12]. This is basically a one layer network used in the unsupervised training algorithm to extract a statistical property of the input data (Fig. 12). At the first step all input data is normalized so the length of each input vector is the same, and usually equal to unity. The activation

functions of neurons are unipolar and continuous. The learning process starts with a weight initialization to small random values. During the learning process the weights are changed only for the neuron with highest value on the output - the winner

$$\Delta \mathbf{w}_w = c (\mathbf{x} - \mathbf{w}_w)$$

where \mathbf{w}_w are weights of the winning neuron, \mathbf{x} is the input vector, and c is the learning constant.

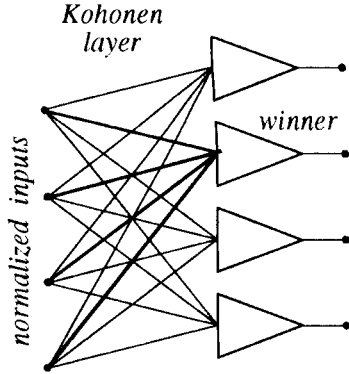


Fig. 12 A winner take all - WTA architecture for cluster extracting in the unsupervised training mode: (a) network connections, (b) single layer network arranged into a hexagonal shape.

The algorithm is modified in such a way that not only the winning neuron, but also neighboring neurons are allowed for the weight change. At the same time, the learning constant c decreases with the distance from the winning neuron. After such a unsupervised training procedure, the Kohonen layer is able to organize data into clusters.

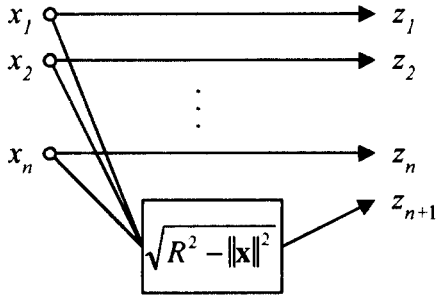


Fig. 13. Input pattern transformation on a sphere

D. Projection on sphere

There are various method to transform input space onto hypersphere without necessity of the information lost [11][21]. In every case the dimensionality of the problem must be increased. The simplest way of transforming input space into hypersphere is to use all input variables untouched $z_i = x_i$ and add one additional input z_{n+1}

$$z_i = \begin{cases} x_i & (i = 1, 2, \dots, n) \\ \sqrt{R^2 - \|\mathbf{x}\|^2} & (i = n + 1) \end{cases}$$

where $\|\mathbf{x}\|^2 = \mathbf{x}^T \mathbf{x}$ is the norm (length) of the input vector. Note that $\mathbf{z}^T \mathbf{z} = \text{const}$ therefore all patterns in new

transformed z -space are located on a hypersphere with radius R .

$$\mathbf{z}^T \mathbf{z} = x_1^2 + x_2^2 + \dots + x_n^2 + \left(\sqrt{R^2 - x_1^2 - x_2^2 - \dots - x_n^2} \right)^2 = R^2$$

The patterns of the transformed z space have the same magnitude and lie on the $n+1$ hypersphere. Each cluster can now be cut off by a single hyperplane in the $n+1$ dimensional space. The separation hyperplanes should be normal to the vectors specified by the clusters' centers of gravity \mathbf{z}_k . Equations for the separating plane of the k -th cluster can be easily found using the point and normal vector formula:

$$\mathbf{z}_k^T (\mathbf{z} - \mathbf{z}_k) = 0$$

where \mathbf{z}_k is the center of gravity of the vector and \mathbf{z}_k is a point transformed from the edge of the cluster. To visualize the problem let us consider a simple two dimensional example with the clusters shown in Fig. 14.

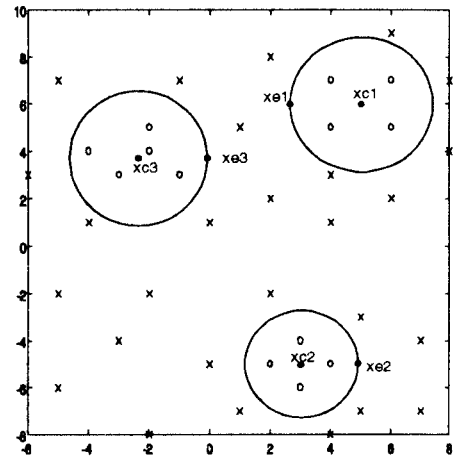


Fig. 14. Separation of three clusters in input space where centers of clusters and edges are marked.

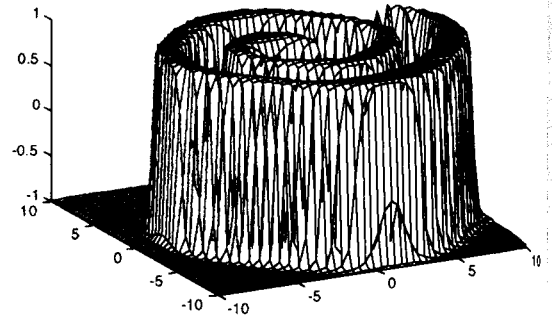


Fig. 15. Spiral problem solved with sigmoidal type neurons

E. Sarajedini and Hecht-Nielsen network

Let us consider stored vector \mathbf{w} and input pattern \mathbf{x} . Both input and stored patterns have the same dimension. The square Euclidean distance between \mathbf{x} and \mathbf{w} is:

$$\|\mathbf{x} - \mathbf{w}\|^2 = (x_1 - w_1)^2 + (x_2 - w_2)^2 + \dots + (x_n - w_n)^2$$

After defactorization

$$\|x - w\|^2 = x_1^2 + x_2^2 + \dots + x_n^2 + w_1^2 + w_2^2 + \dots + w_n^2 - 2(x_1 w_1 + x_2 w_2 + \dots + x_n w_n)$$

finally

$$\|x - w\|^2 = x^T x + w^T w - 2x^T w = \|x\|^2 + \|w\|^2 - 2net$$

where $net = x^T w$ is the weighted sum of input signals. Fig. 16 shows the network which is capable of calculating the square of Euclidean distance between input vector x and stored pattern w .

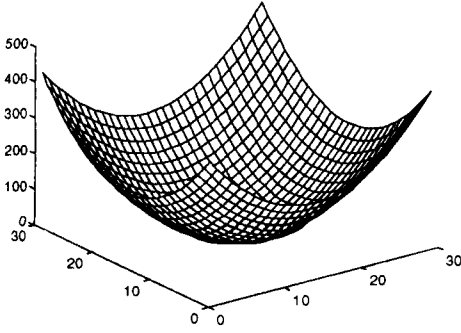
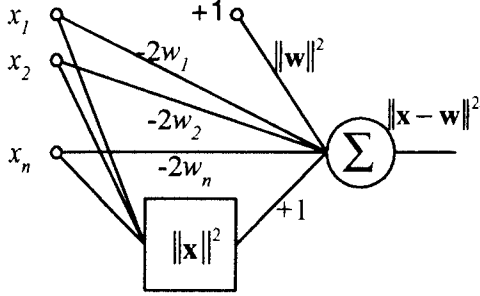


Fig. 16. Network capable of calculating square of Euclidean distance between input pattern x and stored pattern w . (a) network; (b) Euclidean distance calculation form vector (15,15)

In order to calculate the square of Euclidean distance the following modifications are required: (i) bias equal to $\|w\|^2$, (ii) additional input with square of input vector magnitude $\|x\|^2$, and (iii) weights equal to components of stored vector multiplied by -2 factor. Note that when additional input with the square of magnitude is added than simple weighted sum type of network is capable of calculating square of Euclidean distance between x and w . This approach can be directly incorporated into RBF and LVQ networks.

With the approach presented on Fig. 2 several neural network techniques such as RBF, LVQ, and GR can be used with classical weighted sum type neurons without necessity of computing the Euclidean distance in a traditional way. All what is required is to add additional input with the magnitude of the input pattern.

E. Cascade correlation architecture

The cascade correlation architecture was proposed by Fahlman and Lebiere in 1990. The process of network building starts with a one layer neural network and hidden neurons are added as needed. The network architecture is shown in Fig. 17.

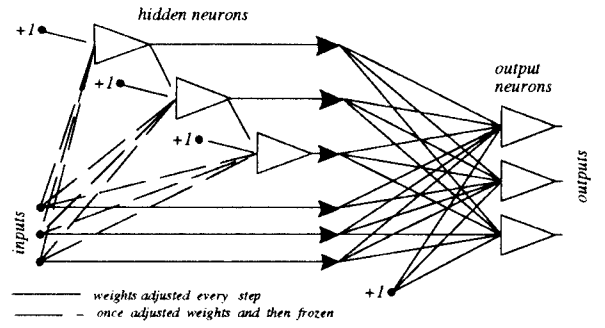


Fig. 17. The cascade correlation architecture.

In each training step, the new hidden neuron is added and its weights are adjusted to maximize the magnitude of the correlation between the new hidden neuron output and the residual error signal on the network output that we are trying to eliminate. The correlation parameter S defined below must be maximized

$$S = \sum_{o=1}^O \left| \sum_{p=1}^P (V_p - \bar{V}) (E_{po} - \bar{E}_o) \right|$$

where O is the number of network outputs, P is the number of training patterns, V_p is output on the new hidden neuron, and E_{po} is the error on the network output. By finding the gradient, $\delta S / \delta w_i$, the weight adjustment for the new neuron can be found as

$$\Delta w_i = \sum_{o=1}^O \sum_{p=1}^P \sigma_o (E_{po} - \bar{E}_o) f_{p'} x_{ip}$$

where σ_o is the sign of the correlation between the new neuron output value and network output, $f_{p'}$ is the derivative of activation function for pattern p , and x_{ip} is the input signal. The output neurons are trained using the delta (backpropagation) algorithm. Each hidden neuron is trained just once and then its weights are frozen. The network learning and building process is completed when satisfied results are obtained.

G. Radial basis function networks

The structure of the radial basis network is shown in Fig. 16. This type of network usually has only one hidden layer with special "neurons". Each of these "neurons" responds only to the inputs signals close to the stored pattern. The output signal h_i of the i -th hidden "neuron" is computed using formula

$$h_i = \exp\left(-\frac{\|x - s_i\|^2}{2\sigma_i^2}\right)$$

where x is the input vector, s_i is the stored pattern representing the center of the i cluster, and σ_i is the radius of this cluster. Note, that the behavior of this "neuron" significantly differs from the biological neuron. In this "neuron", excitation is not a function of the weighted sum of the input signals. Instead, the distance between the input and stored pattern is computed. If this distance is zero then the "neuron" responds with a maximum output magnitude equal

to one. This "neuron" is capable of recognizing certain patterns and generating output signals being functions of a similarity. Features of this "neuron" are much more powerful than a neuron used in the backpropagation networks. As a consequence, a network made of such "neurons" is also more powerful.

If the input signal is the same as a pattern stored in a neuron, then this "neuron" responds with 1 and remaining "neurons" have 0 on the output, as it is illustrated in Fig. 16. Thus, output signals are exactly equal to the weights coming out from the active "neuron". This way, if the number of "neurons" in the hidden layer is large, then any input output mapping can be obtained. Unfortunately, it may also happen that for some patterns several "neurons" in the first layer will respond with a non-zero signal. For a proper approximation the sum of all signals from hidden layer should be equal to one. In order to meet this requirement, output signals are often normalized as shown in Fig. 18.

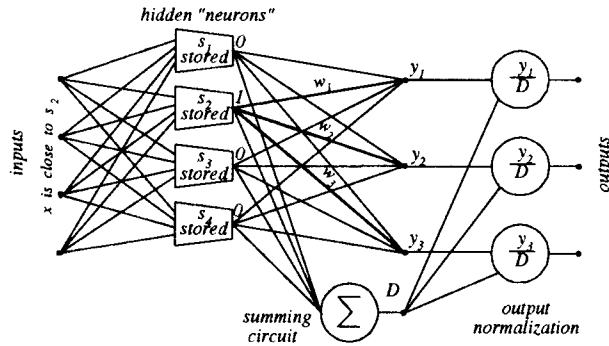


Fig. 18 A typical structure of the radial basis function network.

The radial based networks could be designed, or trained. Training is usually carried on in two steps. In the first step the hidden layer is usually trained in the unsupervised mode for choosing best patterns for cluster representation. A similar approach, as used in the WTA architecture, can be used. Also in this step, radiuses σ_i must be found for a proper overlapping of clusters. The second step of training is the error backpropagation algorithm carried on only for the output layer. Since this is a supervised algorithm for one layer only, the training is very rapid, 100 or 1000 times faster than in the backpropagation multilayer network. This makes the radial basis-function network very attractive. Also, this network can be easily modeled using digital computers, however, its hardware implementation would be very difficult.

VI. RECURRENT NEURAL NETWORKS

In contrast to feedforward neural networks, *recurrent networks* neuron outputs could be connected with their inputs. Thus, signals in the network can continuously circulated. Until now only a limited number of recurrent neural networks were described.

A. Hopfield network

The single layer recurrent network was analyzed by Hopfield in 1982. This network shown in Fig. 19 has

unipolar hard threshold neurons with outputs equal to 0 or 1. Weights are given by a symmetrical square matrix W with zero elements ($w_{ij} = 0$ for $i=j$) on the main diagonal. The stability of the system is usually analyzed by means of the *energy function*

$$E = -\frac{1}{2} \sum_{i=1}^N \sum_{j=1}^N w_{ij} v_i v_j$$

$$\Delta w_{ij} = \Delta w_{ji} = (2v_i - 1)(2v_j - 1)$$

It was proved that during signal circulation the energy E of the network decreases and system converges to the stable points. This is especially true when values of system output are updated in the asynchronous mode. This means that at the given cycle, only one random output can be changed to the required value. Hopfield also proved that those stable points to which the system converges can be programmed by adjusting the weights using a modified Hebbian rule. Such memory has limited storage capacity. Based on experiments Hopfield estimated that the maximum number of stored patterns is $0.15N$, where N is the number of neurons.

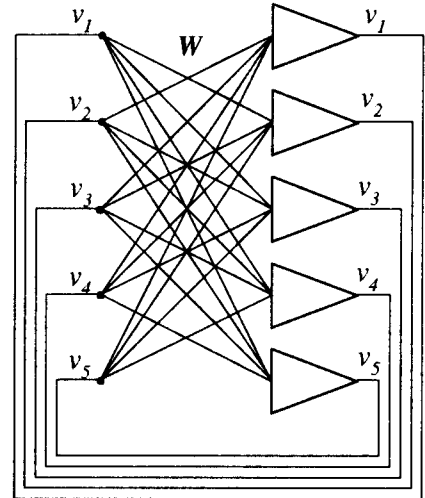


Fig. 19 A Hopfield network or autoassociative memory

B. Autoassociative memory

Hopfield in 1984 extended the concept of his network to autoassociative memories. In the same network structure as shown in Fig. 19, the bipolar neurons were used with outputs equal to -1 or +1. In this network pattern s_m are stored into the weight matrix W using autocorrelation algorithm

$$W = \sum_{m=1}^M s_m s_m^T - M I$$

where M is the number of stored pattern, and I is the unity matrix. Note, that W is the square symmetrical matrix with elements on the main diagonal equal to zero ($w_{ii} = 0$ for $i=j$). Using a modified formula, new patterns can be added or subtracted from memory. When such memory is exposed to a binary bipolar pattern by enforcing the initial network states,

then after signal circulation the network will converge to the closest (most similar) stored pattern or to its complement. This stable point will be at

$$E(v) = -\frac{1}{2} v^T W v$$

the closest minimum of the energy function. Like the Hopfield network, the autoassociative memory has limited storage capacity, which is estimated to be about $M_{max}=0.15N$. When the number of stored patterns is large and close to the memory capacity, the network has a tendency to converge to spurious states which were not stored. These spurious states are additional minima of the energy function.

C. BAM

The concept of the autoassociative memory was extended to bi-directional associative memories - BAM by Kosko [7][8]. This memory shown in Fig. 20 is able to associate pairs of the patterns a and b . This is the two layer network with the output of the second layer connected directly to the input of the first layer. The weight matrix of the second layer is W^T and it is W for the first layer. The rectangular weight matrix W is obtained as a sum of the cross correlation matrixes

$$W = \sum_{m=1}^M a_m b_m$$

where M is the number of stored pairs, and a_m and b_m are the stored vector pairs. If the nodes a or b are initialized with a vector similar to the stored one, then after signal circulation, both stored patterns a_m and b_m should be recovered. The BAM has similar limited memory capacity and memory corruption problems as the autoassociative memory. The BAM concept can be extended for association of three or more vectors.

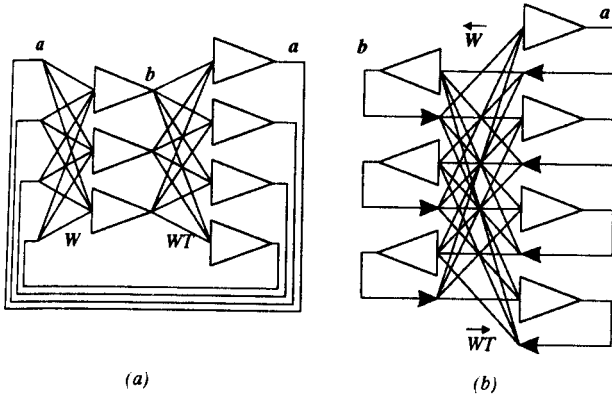


Fig. 20. An example of the bi-directional autoassociative memory - BAM: (a) drawn as a two layer network with circulating signals (b) drawn as two layer network with bi-directional signal flow.

VII. FUZZY SYSTEMS

A. Fuzzy variables and basic operations

In contrary to the Boolean logic where variables can have only binary states, in fuzzy logic all variables may have any

values between zero and one. The fuzzy logic consists of the same basic \wedge - AND, \vee - OR, and NOT operators:

$A \wedge B \wedge C \implies \min\{A, B, C\}$ - smallest value of A or B or C

$A \vee B \vee C \implies \max\{A, B, C\}$ - largest value of A or B or C

$\bar{A} \implies 1 - A$ - one minus value of A

For example $0.1 \wedge 0.8 \wedge 0.4 = 0.1$, $0.1 \vee 0.8 \vee 0.4 = 0.8$, and $\bar{0.3} = 0.7$. The above rules are also known as Zadeh [25] AND, OR, and NOT operators. One can note that these rules are true also for classical binary logic.

B. Fuzzy controllers and basic blocks

The principle of operation of the fuzzy controller significantly differs from neural networks. The block diagram of a fuzzy controller is shown in Fig. 21.(a) In the first step, analog inputs are converted into a set of fuzzy variables. In this step usually for each analog input 3 to 9 fuzzy variables are generated. Each fuzzy variable has an analog value between zero and one. In the next step a fuzzy logic is applied to the input fuzzy variables and a resulting set of the output variables is generated. In the last step, known as defuzzification, from a set of output fuzzy variables, one or more output analog variables are generated, which are to be used as control variables.

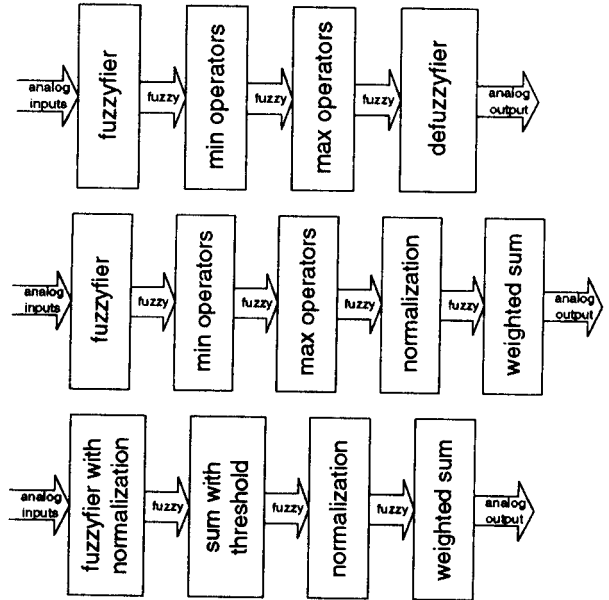


Fig. 21. Typical fuzzy systems (a) proposed by Zadeh, (b) proposed by Takagi-Sugano, and (c) suitable for VLSI implementation

C. Fuzzification

The purpose of fuzzification is to convert an analog variable input into a set of fuzzy variables. For higher accuracy more fuzzy variables will be chosen. To illustrate the fuzzification process let us consider that the input variable is the temperature, and this is coded into five fuzzy variables: cold, cool, normal, warm, and hot. Each fuzzy variable should obtain a value between zero and one, which describes a degree of association of the analog input (temperature) within the given fuzzy variable. Sometimes, instead of term of degree of association, the degree of membership is used. The process of

fuzzification is illustrated in Fig. 22. For example, for a temperature of 57°F , the following set of fuzzy variables is obtained: $[0, 0.5, 0.3, 0, 0]$, and for $T=80^\circ\text{F}$ it is $[0, 0, 0.2, 0.7, 0]$. Usually only one or two fuzzy variables have a value different than zero. In the presented example, trapezoidal function are used for calculation of the degree of association. Various different functions as the triangular, or the gaussian can also be used, as long as the computed value is in the range from zero to one. Always each membership function is described by only three or four parameters, which have to be stored in memory.

For proper design of the fuzzification stage, certain practical rules should be used:

1. Each point of the input analog variable should belong to at least one and no more then two membership functions.
2. For overlapping functions, the sum of two membership functions must not be larger than one. This also means that overlaps must not cross the points of maximum values (ones).
3. For higher accuracy, more membership function should be used. However, very dense functions lead to a frequent system reaction, and sometimes to a system instability.

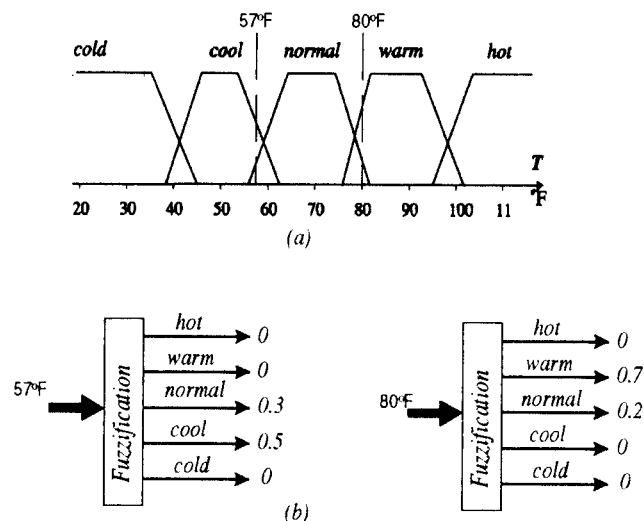


Fig. 20. Fuzzification process: (a) a typical membership functions for the fuzzification and the defuzzification processes, (b) examples of converting a temperature into fuzzy variables.

	y_1	y_2	y_3
x_1	z_1	z_1	z_2
x_2	z_1	z_2	z_3
x_3	z_1	z_3	z_4
x_4	z_2	z_3	z_4
x_5	z_2	z_3	z_4

(a)

	y_1	y_2	y_3
x_1	t_{11}	t_{12}	t_{13}
x_2	t_{21}	t_{22}	t_{23}
x_3	t_{31}	t_{32}	t_{33}
x_4	t_{41}	t_{42}	t_{43}
x_5	t_{51}	t_{52}	t_{53}

(b)

Fig. 21. Fuzzy tables: (a) a table with fuzzy rules, (b) table with the intermediate variables t_{ij} .

D. Rule Evaluation

Fuzzy rules are specified in the fuzzy table as it is shown for a given system. Let us consider a simple system with two analog input variables x and y , and one output variable z . We have to design a fuzzy system generating z as $f(x, y)$. Let us also assume that after fuzzification the analog variable x is represented by five fuzzy variables: x_1, x_2, x_3, x_4, x_5 and an analog variable y is represented by three fuzzy variables: y_1, y_2, y_3 . Let us also assume that an analog output variable is represented by four fuzzy variables: z_1, z_2, z_3, z_4 . The key issue of the design process is to set proper output fuzzy variables for all combination of input fuzzy variables, as it is illustrated in the table (a) shown in Fig. 23. The designer has to specify many rules such as: if inputs are represented by fuzzy variables x_i and y_j then the output should be represented by fuzzy variable z_k . It is only required to specify what membership functions of the output variable are associated with a combination of input fuzzy variables. Once the fuzzy table is specified, the fuzzy logic computation is proceeded with two steps. At first each field of the fuzzy table is filled with intermediate fuzzy variables t_{ij} , obtained from AND operator $t_{ij} = \min\{x_i, y_j\}$, as shown in Fig. 23(b). This step is independent of the required rules for a given system. In the second step the OR (max) operator is used to compute each output fuzzy variable z_k . In the given example in Fig. 22 $z_1 = \max\{t_{11}, t_{12}, t_{21}, t_{31}\}$, $z_2 = \max\{t_{13}, t_{22}, t_{41}, t_{51}\}$, $z_3 = \max\{t_{23}, t_{32}, t_{42}, t_{52}\}$, $z_4 = \max\{t_{33}, t_{43}, t_{45}\}$. Note that the formulas depend on the specifications given in the fuzzy table shown in Fig 23(a).

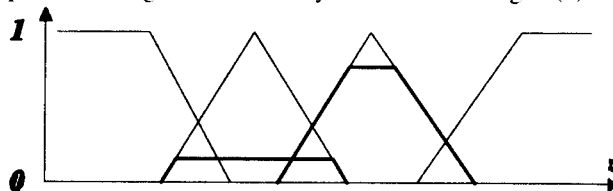


Fig. 24. Illustration of the defuzzification process.

E. Defuzzification

As a result of fuzzy rule evaluation, each analog output variable is represented by several fuzzy variables. The purpose of defuzzification is to obtain analog outputs. This can be done by using the similar membership function as shown in Fig. 22. In the first step fuzzy variables obtained from rule evaluations are used to modify the membership function employing the formula

$$\mu_k^*(z) = \min\{\mu_k(z), z_k\}$$

For example, if output fuzzy variables are: 0, 0.3, 0.7, 0.0, then the modified membership functions have shapes shown by the thick line in Fig. 24. The analog value of the z variable is found as a "center of gravity" of modified membership functions $\mu_k^*(z)$

$$z_{analog} = \frac{\sum_{k=1}^n \int_{-\infty}^{+\infty} \mu_k^*(z) z dz}{\sum_{k=1}^n \int_{-\infty}^{+\infty} \mu_k^*(z) dz}$$

In the case when shapes of the output membership functions $\mu_k(z)$ are the same, the above equation can be simplified to

$$z_{analog} = \frac{\sum_{k=1}^n z_k z_{Ck}}{\sum_{k=1}^n z_k}$$

where n is the number of membership function of z_{analog} output variable, z_k is fuzzy output variables obtained from rule evaluation, and z_{Ck} are analog values corresponding to the center of k -th membership function.

VIII. VLSI FUZZY CHIP

The classical approach to fuzzy systems presented by Zadeh [25] is difficult to implement in analog hardware. Especially difficult is the defuzzifier where signal division must be implemented. Division can be avoided through use of feedback loops, but this approach can lead to limited accuracy and stability problems. Also, in the case of the classical fuzzy system shown in Fig. 21, the information about the required control surface is encoded in three places: in the fuzzifier, in the defuzzifier, and in the prewired connections between MIN and MAX operators. Although the architecture is relatively simple, it is not suitable for custom programming.

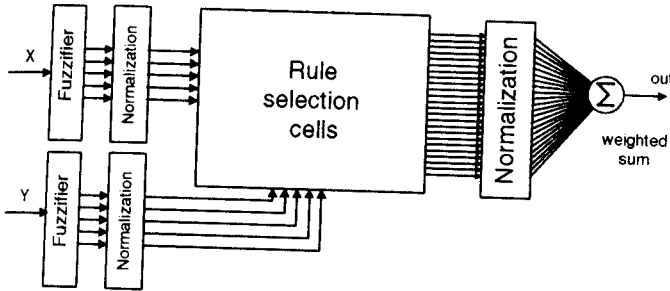


Fig. 25. Architecture of VLSI fuzzy controller

The concept of the proposed circuit is shown in Fig. 25. Fuzzification is done in a traditional manner with additional normalization which leads to a linear interpolation of the output between stored points. The second stage is an array of cluster cells with fuzzy "AND" operators. Instead of classical defuzzification, simplified Takagi-Sugeno singleton inference rules [18] with normalization are used. The output is then normalized and calculated as a weighted sum of the signals approaching from all selected areas.

A. Fuzzifier

Various fuzzifier circuits that can be implemented in bipolar or MOS technology have already been proposed. Most approaches use two source- or emitter-coupled differential pairs for a single membership function. The approach proposed here differs from the previous techniques in two ways (i) it is simpler - only one differential pair is required per membership function and (ii) the fuzzy outputs are automatically normalized;

therefore the sum of all the signals representing the fuzzy variables of a single input is constant.

The fuzzifier circuit is presented in Fig. 26. This design requires only one differential pair for each membership function, in contrast to earlier designs where at least two differential pairs were required. Also the output currents are automatically normalized because the sum of I_1 through I_6 is always equal to I_0 . Thus the normalization circuit is integrated within the fuzzifier.

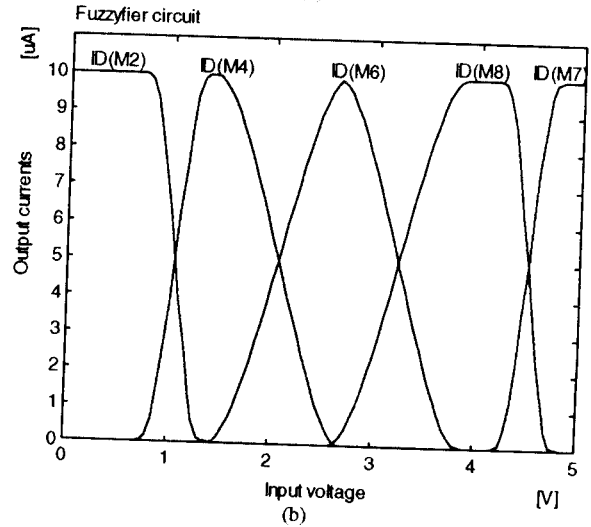
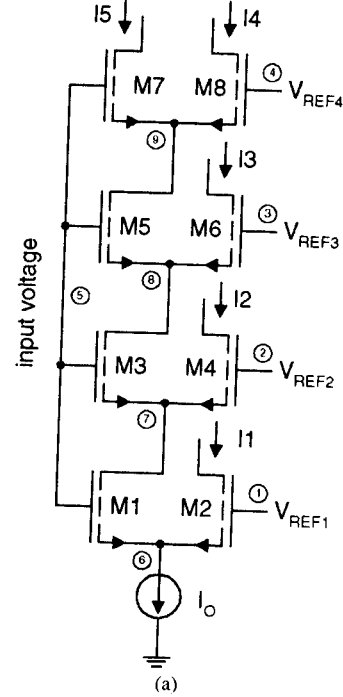


Fig. 26. Fuzzyfier circuit with four differential pairs creating five membership functions: three Gaussian/trapezoidal-like in the center and two sigmoidal types at the ends of the input range: (a) circuit diagram and (b) fuzzyfier characteristics generated by SPICE program.

B. Array of Rule Selection Circuits

Each rule selection circuit is connected to one fuzzy variable from each fuzzifier. Therefore the number of these circuits is equal to $n_1 * n_2$, where n_1 and n_2 are numbers of fuzzy variables for each fuzzifier. The rule selection circuit cell is activated only if both fuzzy inputs have non-zero

values. Due to the specific shapes of the fuzzifier membership functions, where only two membership functions can overlap, a maximum of four cluster cells are active at a time. Although current mode MIN and MAX operators are possible, it is much easier to convert currents from the fuzzifiers into voltages and use the simple rule selection circuits with the fuzzy conjunction (AND) or fuzzy MIN operator.

The voltage on the common node of all sources always follows the highest potential of any of the transistor gates, so it operates as a MAX/OR circuit. However using the negative signal convention (lower voltages for higher signals) this circuit performs the MIN/AND function. This means that the output signal is low only when all inputs are low. A cluster is selected when all fuzzy signals are significantly lower than the positive battery voltage. Selectivity of the circuit increases with larger W/L ratios. Transistor M3 would be required only if three fuzzifier circuits were used with three inputs.

C. Normalization circuit

In order to obtain proper outputs it is essential that normalization occurs before weights are applied to the summed currents. The normalization circuit can be implemented using the same concept as the rule selection circuit. For the negative signal convention, PMOS transistors supplied by a common current source are required. The normalization circuit is shown in Fig. 27. The voltage on the common node A follows the lowest input potential. The normalization circuit consists of transistors M1, M2, ...M_N connected to a single common current source. This means that the sum of all drain currents of transistors M1, M2, ...M_N is always constant and equal to I_D . The W/L ratios in current mirrors can determine the output value for each cluster. Currents from all cluster cells are summed to form the output current.

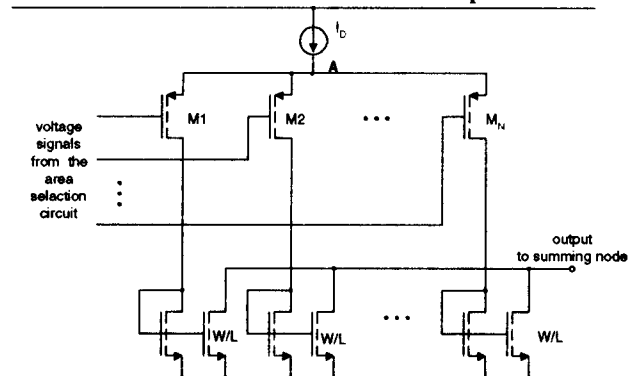


Fig. 27. Normalization circuit

D. Weight circuit

The weights for different clusters can be assigned by setting proper W/L ratios for current sources. This task can be also accomplished by introducing a digitally programmable current mirrors.

E. VLSI implementation

A universal fuzzy approximator has been designed and fabricated. In order to make the chip universal, each fuzzifier consists of seven differential pairs with seven equally spaced reference voltages. This results in eight membership functions for each input and $8 \times 8 = 64$ cluster cells. Sixty-four adjustable current mirrors for setting weights of output signals are programmed with 6 bits accuracy. For an arbitrary two-dimensional function only $6 \times 64 = 384$ bits are required for programming. A test chip has been implemented in the $2 \mu\text{m}$ n-well MOSIS process using more than 2000 transistors to perform the analog signal processing. To simplify the test chip implementation, current sources were programmed at the contact mask level. Fig. 28 shows a comparison between the desired and the actually measured control surface from the fuzzy chip.

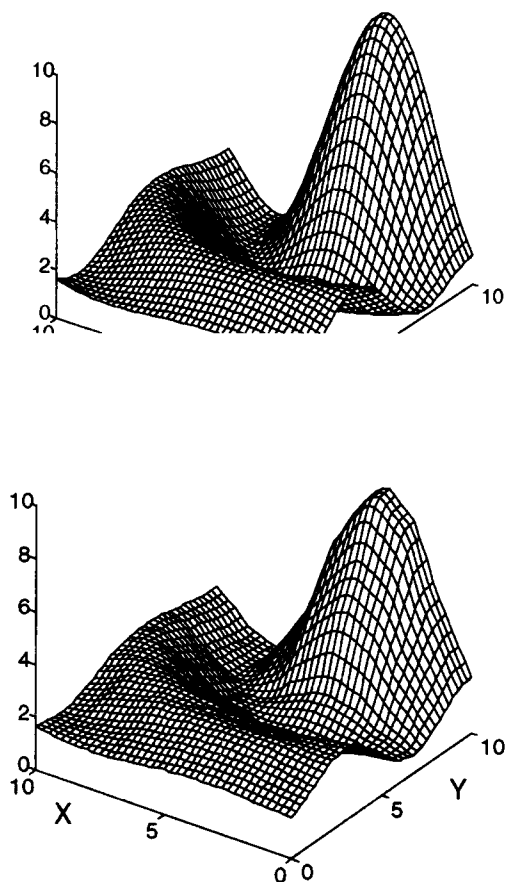


Fig. 28. Control surfaces: (a) desired control surface, and (b) example of a control surface obtained from fuzzy system using weights

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Analog VLSI hardware for fuzzy systems

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Abstract - Our world has an analog nature and it is natural to process signals in an analog way. Analog signal processing can be much faster than digital ones and AD or DA conversion is not required. The main obstacle is to develop adequate circuits for nonlinear signal processing. In the presentation several new circuits are proposed. These circuits use nonlinear characteristics of MOS transistors for nonlinear signal processing. The fuzzy signal processing is used as an example. The proposed fuzzyfier circuits are relatively simple while almost arbitrary shapes of membership functions can be obtained. The proposed current mode MAX and MIN operators exhibit accuracy superior to other circuits. The defuzzyfier circuit uses the concept of signal normalization and weighted sum. New normalization circuit, operating in the subthreshold conduction mode, exhibits almost ideal characteristics. The described new building blocks were used to design the entire analog fuzzy VLSI chip.

I. INTRODUCTION

Numerous applications of industrial electronics use intelligent control systems. For example many motor control systems require sophisticated computation. The intelligence is also involved in smart sensors that are able to measure flux and other electrical parameters just by analyzing currents and voltages on the supply terminals. Hardware implementation of intelligent systems use computers or microcomputers for the computation. The digital approach has many advantages. Primarily it is flexible and easy to be reprogrammed. At the same time those digital systems are rather complex and they require analog to digital conversion at the front of the system and digital to analog conversion at its end. Our world has an analog nature and it would be wise to perform all computation in analog fashion.

Analog signal processing is usually much faster than the digital one. Several computation processes can be done simultaneously, and AD and DA conversion is not required. Analog integration or differentiation have been used for many years already. Also, analog summation and multiplication are quite common. For intelligence computation more sophisticated nonlinear functions are required such as WTA (Winner Takes All), fuzzy membership functions [1][3][6][9][14][16], normalization circuits [5][10][11][14], MIN and MAX operators [2][3][7][8][12] division circuits, analog memory, neural circuits, and others. This presentation is focused on the VLSI implementation of fuzzy systems. Nonlinear signal processing is taking advantage of nonlinear characteristics of MOS devices. All building blocks operate in current

mode, which means that the current not voltage carries information.

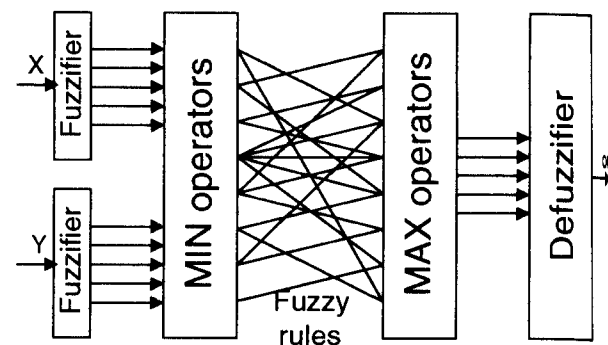


Fig. 1 Classical Zadeh-type fuzzy controller

The block diagram of the classical Zadeh-type [17] fuzzy system is shown in Fig. 1. The system consists of fuzzyifiers, a main processing unit with MIN and MAX operators, and a defuzzyfier. Several VLSI implementations of fuzzy system were already presented [10][15]. The approach in [15] uses voltage mode computation and Tagagi-Sugeno defuzzification [13], which leads to simple programming, but requires many transistors for implementation. In the presented approach, several new circuit analog signal processing circuits are described. Those circuits are often simpler and have better characteristics.

II. FUZZYFIERS

The fuzzyfier block must convert crisp analog values into several fuzzy variables. The conversion takes place based on the dedicated membership functions of triangular, trapezoidal or Gaussian type shapes. Several different circuits have been already proposed. Ahmadi et al [1] used PWL approximations using current sources. Yamagawa [16] used bipolar technology. Ota and Wilamowski [9] used a similar approach, but MOS transistors were used. In both cases, for a single membership function, two differential pairs were required. Also those differential pairs had to be supplied by several identical current sources. In the proposed approach only one differential pair is required per membership function and one current source per fuzzyfier.

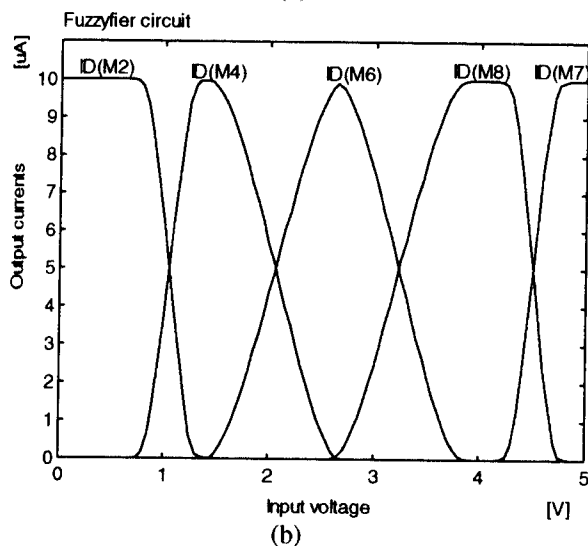
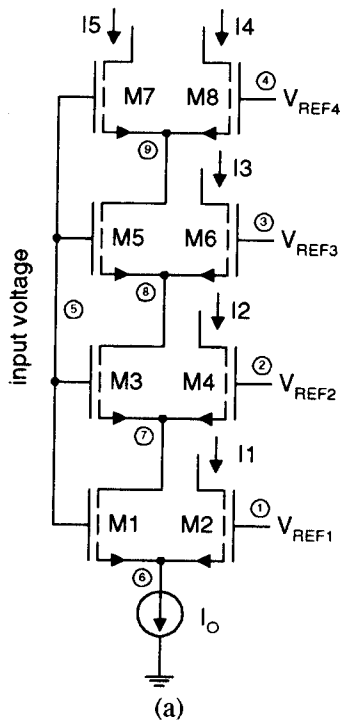


Fig. 2 Fuzzyfier circuit with four differential pairs creating five membership functions: three Gaussian/trapezoidal-like in the center and two sigmoidal types at the ends of the input range: (a) circuit diagram and (b) fuzzyfier characteristics generated by SPICE program.

The circuit diagram of the fuzzyfier is shown in Fig. 2(a), while the fuzzyfier characteristics are shown in Fig. 2(b). In the example shown in Fig. 2(b) transistors M1, M2, M7, and M8 have $W/L = 10\mu\text{m}/2\mu\text{m}$, while others have $W/L = 4\mu\text{m}/4\mu\text{m}$. Transistor models of typical $2\mu\text{m}$ n-well MOSIS process were used in the simulation. The reference voltages were set to 1.0V, 2.0V, 3.2V, and 4.5V. Note that several different shapes of the membership function can be obtained such as: triangular, trapezoidal, and Gaussian. Reference voltages control the width of membership functions, while slopes depend on the W/L ratio of coterminous differential pairs.

III. MIN AND MAX OPERATORS

The voltage mode MIN and MAX operations are very simple to implement [14][15][16]. All that it is required to have several voltage followers circuits with all emitters/sources shorted together. Those simple voltage mode MIN and MAX operator have limited accuracy and can operate only in relatively low signal ranges. Current mode MIN/MAX operators operate correctly over several orders of magnitude of signal change. Baturone et al [4] proposed a very clever current mode MAX operator with relatively high accuracy (Fig. 3). The presented here MAX operator is equally simple, but even higher accuracy can be obtained (Fig 4). Its performance is illustrated in Fig. 5. Each MAX circuit can be easily converted into a MIN circuit by introducing additional biasing currents as it is shown in Fig. 6.

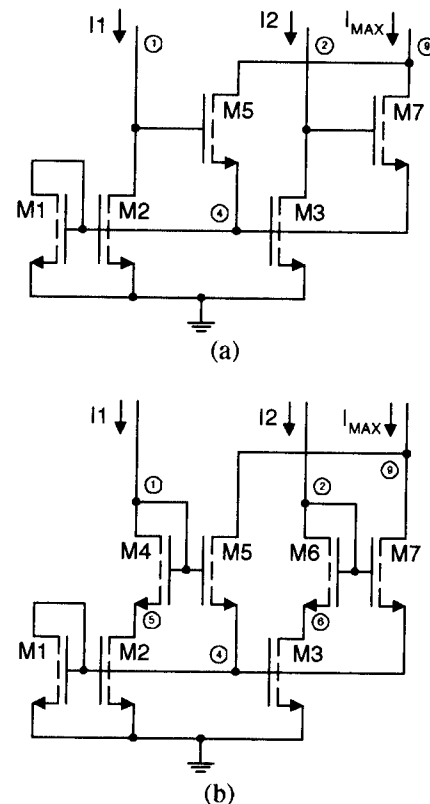
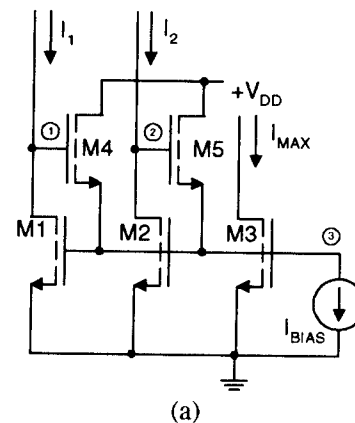


Fig. 3. Baturone MAX operator circuit for two inputs (a) concept diagram and (b) actual implementation.



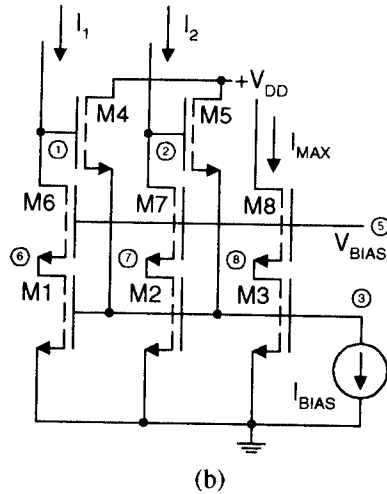


Fig. 4. Proposed MAX operator (a) concept diagram and (b) actual implementation.

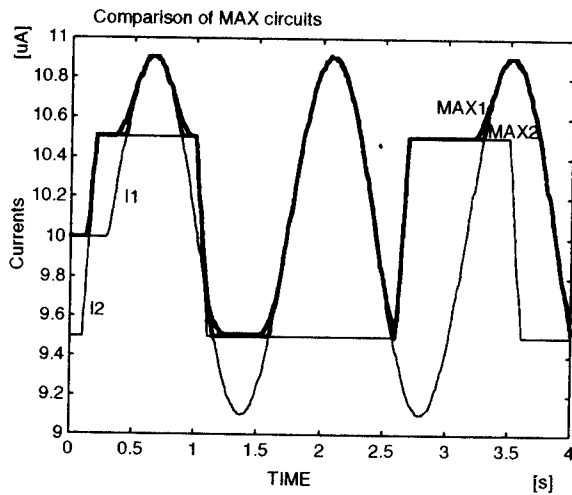
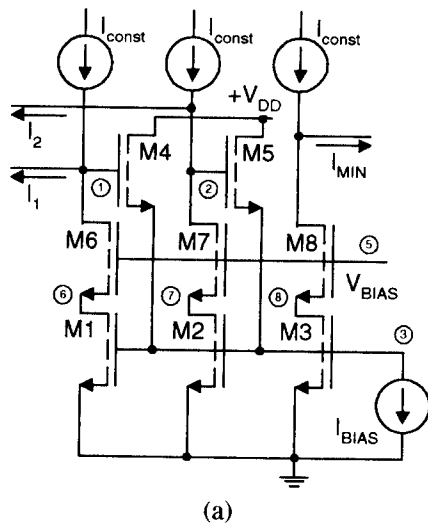


Fig. 5. Accuracy comparison of Baturone MAX1 and new proposed MAX2 circuits



(a)

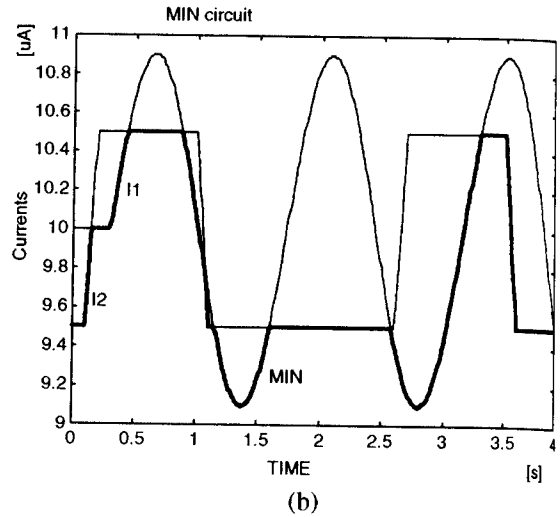


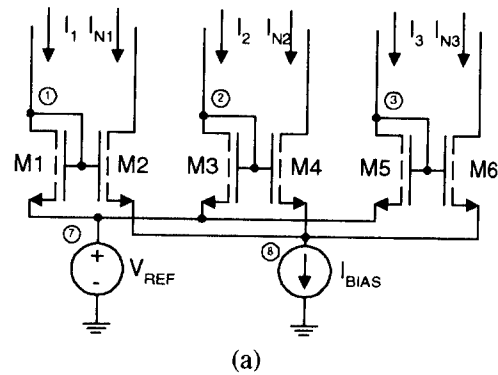
Fig. 6. SPICE simulation results for MAX and MIN circuits (a) accuracy comparison of Baturone et al and new proposed MAX circuits and (b) Results of proposed MIN circuit.

VI. DEFUZZIFIER CIRCUIT

The simplest defuzzification circuit should perform the following computation:

$$OUT = \frac{w_1 x_1 + w_2 x_2 + \dots + w_n x_n}{x_1 + x_2 + \dots + x_n}$$

The required division is very difficult to implement in VLSI. Several attempts have already been made to substitute division by another technique. When negative feedback approach [7][11] is used, only the effect of dominant inputs (inputs with large signals) is calculated correctly. A similar problem exists, when normalization techniques as described in [10] are used. In order to avoid the defuzzification Takagi-Sugeno [13] was used in the VLSI implementation [15]. Fig. 7 shows new normalization circuit which uses MOS transistors operating in subthreshold conduction mode. The normalization concept requires a gain control on all input signal paths so each signal is attenuated the same way and that the sum of the reduced signals is always constant $x_1 + x_2 + \dots + x_n = const$. Such normalized signals are then summed with weights to complete the defuzzifier circuit.



(a)

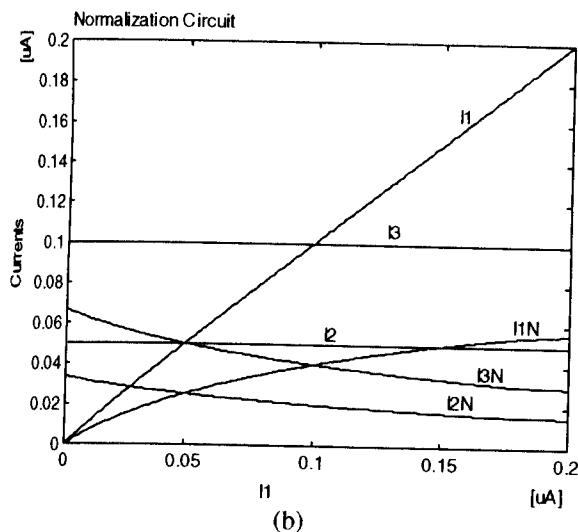


Fig. 7. Normalization circuit with almost ideal characteristics (a) circuit diagram and (b) characteristics

When all transistors of the circuit in Fig. 75. operate in the subthreshold conduction mode the circuit exhibits almost ideal characteristic. This can be also proven analytically. If currents are larger and transistors operate in the strong inversion mode, results are only an approximation of that required. Fig. 7 shows simulation results. Note that sum of normalized currents is always constant and that the same current ratios are preserved after normalization. The latter feature is very difficult to accomplish in other solutions of normalized circuits.

V. CONCLUSION

The presented building blocks such as fuzzyfier, MIN and MAX operators, and defuzzyfier were used in the design of the entire fuzzy VLSI chip. At this point, the designed chip has to be individually designed for each application. The number of membership functions and its shape require proper reference voltages and proper W/L ratios in fuzzyfier circuits. Weights of the defuzzyfier are also adjusted by adequate W/L ratios of current mirrors. Inputs of MIN operators have fixed connections. Fuzzy rules are implemented by proper connections between outputs of MIN operators and inputs of MAX operators.

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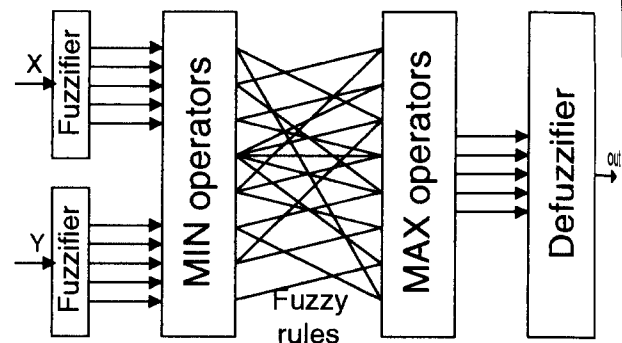


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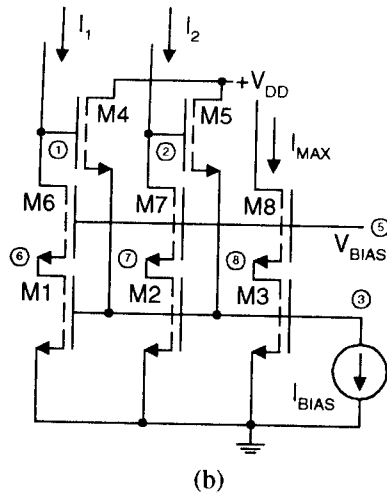


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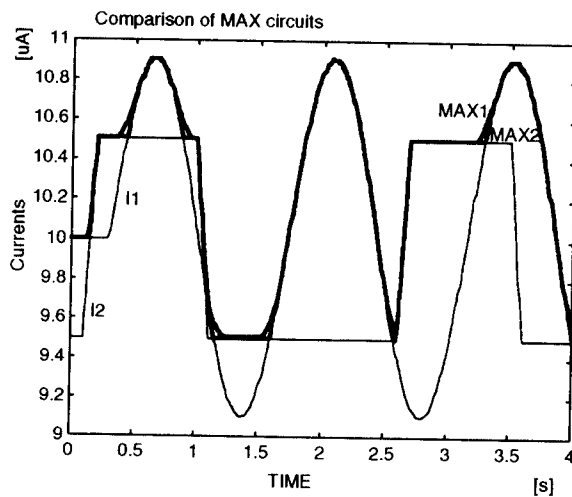
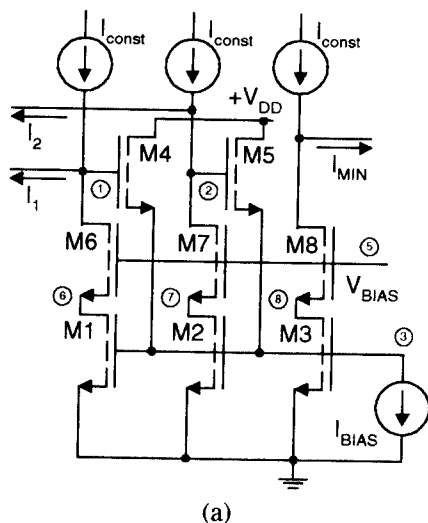


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